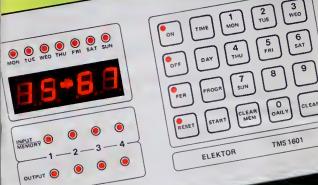


April 1983

ATR-1.14" OPY Swirts Sigk

Programmable timer/controller

The hyboard is part of the



darkroom timer remote control audio compact LCD thermometer



| AIR-MAIL COPY  | -    |  |
|--|------|--|
| selektor   | 4.20 | platata.   |
| low-power digital thermometer E. Scheid The thermomater described in this article can be operated continuously for mora than six months without changing the battery. That is quite an achievement and already makes our last thermometer circuit appear a little obsolete.  | 4-22 | OF THE PARTY OF TH |
| MC/MM phone preamp The phone preamp is an important part of the Prelude, or indeed of any saudio system. Most railly good cartridges are the moving coil type, and thase require a stepup transformer or, as is more common nowedays, a so-called pre-pramp. The phone preamp and the moving coil pre-preamp described in this article are designed to form a single modula. This can be incorporated in any audio system, although it is intended as part of the Prelude. | 4-26 | 15-61<br>9-9-9-6<br>9-9-9-6<br>9-9-9-6   |
| technical answers  National filter with application fault; marking of digitast switches; slava flashgun powered by the mains; Crescendo coils and S/N ratio.   | 4-33 | The front panel for the 7-day<br>timer/controllar includes a<br>mambrane-switch keypad   |
| membrane switches The membrane switch — or foil switch as it is becoming popularly known—is almost too good to be true. The switch is both reliable and inexpensive. These two factors coupled with its "beliened fiction" appearance can provide the basis for a very elegant and economical keyboard that the more conventional switch examot hope to match.   | 4-34 | and red trans lucent windows<br>for the LEDs and display.<br>Anyone who happens to<br>notice something out of the<br>ordinary in the time display<br>is referred to the top right-<br>hand corner of this month's  |
| Interlude. The interlude brings the same armchair operation sets is fairly well-known. The interlude brings the same armchair operation to the world of hi-fri and o'volume, balance, tone, even input select — all controlled from the "ideal listening position".  | 4-36 | cover.   |
| RC equalizer   | 4-40 |  |
| 7-day timer/controller.  The last time we published a single-chip programmable timer was way back in May 1979 and the circuit became very popular (and still is). However, the IC used at the time is now a little "thin on the ground" and it's about time the subject was brought up to date. The circuit here uses the TNS 1801 from Texas Instruments, a single-chip microcomputer specifically designed for this purpose.   | 4-42 | ₩  |
| junior program tester  | 4-51 | & W  |
| Prelude (Part 3)   | 4-52 | Pape   |
| programmable darkroom timer, Many amature photographers will be intarested in this circuit: a programmable timar for seven presilectable times. The time can be adjusted in two ranges (up to 99.9 s and up to 999.9). Fourteen keys are used for programming. When the start kay is pressed, the tima countdown can be observed on the display.   | 4-56 | and was  |
| talking-clock extension  | 4-60 | Jan &  |
| market   | 4-61 | 0  |
| switchboard  | 4-65 |  |
| EPS service  | 4-80 |  |
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| 2 μF             | 1000 pF    | 800 Hz         |  |  |  |
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# Edddle

## The potential of the bucket brigade

Charge coupled devices, or CCDs es they ere usually celled, have evolved through semiconductor technology. Flectronics engineers and technicians often think of them as 'bucketbrigade' elements in a circuit, from the way they work. But CCDs are making e remarkable contribution to such highly sophisticated fields as astronomy and particle physics. Because they are semiconductor devices, CCDs are robust, consume little power, have a long life end ere compact. The unit of length used in describing them is the micrometre, and the extremely small dimensions must be remembered when looking at explanetory diagrams which have to be drawn large. Discussions range round charges of, say, only e thousand or so electrons, wherees a normal n-type doped silicon has some 10<sup>12</sup> electrons per cubic millimetre. With CCDs we are in a minute world of microscopic size. Indeed, devices can have hundreds of thousands of unit cells on a chip hardly more than a square centimetre in aree.

Furthermore, the manufacturing technology is simpler than that for integrated circuits, because it involves very few successive stages. It is based lergely on well-known techniques used in making field-effect transistors.

The basic, simplified CCD is outlined in figure 1, where (e) represents two CCD units, A end B. There is a charge of electrons in A. If the voltage on A is reduced to nothing and thet on B is raised, the electrons travel through the n-type channel towards B. In this way the charge is 'coupled' or trensferred between A and B. Pert (b) is e somewhat more realistic side-elevation, showing the silicon p-type substrate (which has a deficit of electrons) on the top of which is a very thin n-type layer. Over this is an insulation lever of silicon dioxide. Above them all there are the conducting electrodes, which overlap but ere insulated from one another. Depending on the purpose of the device, the thickness of the substrate may be from 35 to 300 µm and the n-type layer may be only a few micrometres thick.

The first, essentiel point to be understood is that the doped layers are depleted for use in CCDs. That is to sey, they are deprived of mobile carriers, which are electrons in the n-type and 'holes', or electron deficits, in the p-type. This is done by applying a reverse bias, or

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Figure 1. Elementary idea of two charge elements coupled together via the n-type channel and controlled by voltages at A and B in sequence (see taxt).

opposing potential, of about 20 V. It ensures that the n-type layer has plenty of space for electrons, often described as a conducting layer or channel. The depleted p-type layer is left negatively charged to a small depth, keeping electrons up into the channel.

The arrangement shown is not of

any practical use, for alternation of the voltages on A and B would merely keep the charges going backwards and forwards between them, However, a linear errangement of a number of elements could be useful if we can have a way of getting the voltages on the electrodes in the correct sequence. This is done by what is called a three-phase system. If we essume a charge of electrons under A (figure 2) and that the potential  $\varphi_1$  is reduced to zero while φ2 is given a voltage, then the charge moves from A to B,  $\varphi_3$  also being zero. Next,  $\varphi_2$  is reduced to zero and φ2 given a voltage, and the charge moves to C. Next,  $\varphi_3$  goes to zero and  $\varphi_1$  is given a voltage. The charge moves to D. So the correct sequence of voltages in time makes the charge move along, eventually reaching J, where it goes to an emplifier and so forth. Obviously, there has to be a clocked sequence of voltages in the right order. It is obvious, too, that there is never a condition where there are two or three charges

contiguous.

The errey so fer considered is eliment one. It can be used in analogue mode, with each charge differing, or digital mode with individual charges representing 1 or 0. It can operate as shift register, a serial access store in computers, e deley line in intercommunications and televisions and relations and seven, suitably modified, or acid successful and a serial section of the control of the co

frontier of several fields of scientific research and development in the lest few years is the idea of making it into a two-dimensional array, an area rather then a line. The reason lies in the fact that charges can be created in several ways. In the linear device, the first charge usually comes from preceding circuits and at exit goes into another circuit. However, when light enters silicon it creates charges by photoelectric effect, and the spectral range of sensitivity is quite wide. Also, if a high-energy particle enters an element of a CCD array it creates a charge by ionization. For these kinds of chargecreation the electrodes have to be trensparent, which is arranged by making them of very highly doped silicon or of polycrystalline silicon instead of metal; they conduct well enough.

#### Ares Array

If we imagine the lineer erray of figure 2 to be up ended, with A et the top, end turned to show the electrode faces, and then add similar arrays side by side, we get the idea of an area arrey (except thet, of course, the whole area is formed on one silicon chip). In addition, in order to restrein the electrons from moving sideways (that is, in the direction of the conventional x-axis in our imagined picture), so-called chennel stops are formed from top to bottom after every three elements. Eech stop is an extremely shallow channel and consists of very heavily doped p-type silicon; when depleted, it becomes a strong, negatively-

Figure 2. Linear arrangement of e CCO, Charge is moved along one element at a time from left to right. [Overlaps of electrodes are omitted for clarity.]





charged barrier. A picture element, or pixel as it is known, then consists of an area covering three CCD elements horizontally and vertically. Obviously, the charges in each pixel cannot be seen. They are transferred, one row at a time, (in a so-called frame-trensfer technique, which is not the only way) to a linear shift register with its own three phase clock pulse supply, so that each pixel charge is transferred individually into circuitry that includes microprocessors: the Cartesian co-ordinates end the size of the charge in each pixel are stored and transferred to a television screen or visual display unit (VDU), on which we can see the picture of how the original light or ionization is disposed, just as if the area CCD array were a photo-

graphic plate. The advantage of such a CCD image sensor for astronomy has been described by one scientist for the UK Science and Engineering Research Council in quite unscientific terms: he says the sensitivity is "staggering". One sensor having 576 x 385 pixels in a surfece of ebout 15 mm x 9 mm is in use et the Royal Observatory, Edinburgh, A CCD sensor has been commissioned for the Anglo-Australian telescope and yet another for the South African Astronomical Observatory. Stars and gelexies extremely difficult or impossible to photograph even with hours of exposure can now be recorded in minutes.

The need in high-energy physics is quite different. As everyone now knows, many particles have been identified and given strange names. with properties such es 'beauty' and 'charm'. Most of them have lifetimes expressible only with the help of

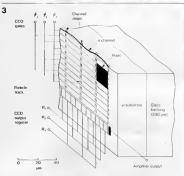
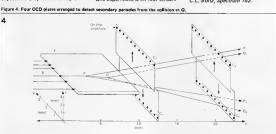


Figure 3. Area array with channel stops to restrain lateral movement of electrons. The pixel, or picture element consists of three CCD elements horizontally end vertically.

large negative indices, for example 10<sup>-12</sup> seconds, so measuring them and their interactions has become more and more exacting. To do the job, highly-sophisticated pieces of apparatus such as the bubble chamber have been devised. Now, with the high sensitivity and resolution of CCD image sensors. together with the easy processing of the results by computers. physicists heve yet another refined tool to use.

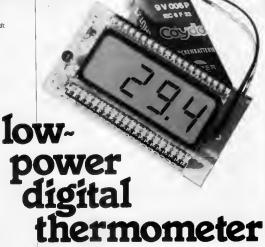
One experiment with four sensors

is shown in figure 4. An energetic particle hits a target. T. At one collision it causes the emission of particles with tracks P1 and P2. Then, perhaps a couple of millimetres further on, it has another collision that gives rise to particle tracks O: and On. Ordinarily it would be impossible to differentiate between the Ps and Qs but with a CCD technique it should be possible to measure what happens with a precision of e few micrometres. C.L. Boltz, Spectrum 182.



low-power digital thermometer elektor april 1983

E. Schmidt



A large number of digital thermometers are available at present. Elektor has also kept up with tha trend and published a standard digital thermometer with liquid crystal display in the October 1982 issue. Why is it that only a few months latar we are coming out with a successor to the circuit already published? Tha thermomater described in this article can be operated continuously for more than six months without changing the battery. That is quite an achievement and already makes our last thermometer circuit appear a little obsoletal

continuous operation for more than 6 months In spite of the wide choice of devices available, digital thermometers have not yet become fully accepted. The advantages are: the temperature can be clearly read off at a distance; the response time is very short and the temperature sensor can be installed at some distance from the unit.

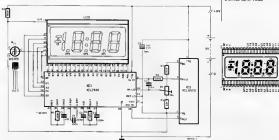
In contrast to the usual mercury and 'pointer' thermometers, however, the electronic version requires its own power source. Of course, it is also much more expensive. It seems, therefore, that digital thermometers will only become more popular when the problem involving price and energy consumption has been at least partially solved. Special ICs are needed for this type of circuit, but the investment is worthwhile.

#### The circuit

In principle, this thermometer consists of two main circuit components (see figure 1). These are the temperature sensor IC2, whose output voltage is proportional to the temperature, and the analogueth-digital converter IC1, which converts this voltage value to the corresponding binary number and also has the task of driving the liquid crystal display.

The analogue-to-digital converter ICL 7136 (IC1) is a chip with an extremely low current consumption, approximately 50  $\mu$ A, which operates according to the dual-slope principle. With a reference voltage of 100 mV which must be applied to it





between REFLO and REFHI, its measuring range is then  $\pm$  199.9 mV.

1

Thus the converter directly indicates with proper polarity the voltage applied to its inputs INLO and INHI. The sign is positive if the potential applied to INH1 is bigher than that at INLO; in the inverse case, a "-" appears on the display. The frequency of the internal oscillator is adjusted with external components C1 and R1. At the specified values, a clock frequency of approximately 16 kHz is obtained; this means that the ICL 7136 executes approximately one conversion per second, Polycarbonate or polyester capacitors should be chosen for C1, C3 and C4 to keep the measuring error of the A/D converter less than 0.1°C (0.1°C corre sponds to 1 LSB). The backplane signal must be inverted to generate the decimal point, This is performed with transistor T1 and resistor R6. An alternative is to utilise a BC 549C with a 4M7 base resistor,

The temperature sensor ICL 8073 (IC2) operates with a supply voltage of approximately 5 V which is presented to it by the A/D converter at the +Up and TEST terminals. The IOO mV reference voltage for het A/D converter is presented by the sensor between UREF and -Up. The I mV/K voltage and IOD and IOD

inequast network consisting of R3 and C5.
A O'C (corresponding to 278 k), therefore, an output voltage of 273 mV is present. As already mentioned, the difference voltage between INH<sub>II</sub> and INI<sub>IO</sub> is indicated by the API converter. Thus a voltage of 273 mV must be presented to the INI<sub>IO</sub> input in a C6. This is achieved by dividing down the reference voltage UB<sub>C</sub> (approximately 1.33 V) via potential divider R4, PI, R5 and 1.33 V) via potential divider R4, PI, R5 and

Figure 1. The circuit of the low-power digital thermometer meinly consists of two special ICs and the figuid crystal display, IC2 operates as a temperature sensor. IC1 evaluates the voltages of IC2 and drives the LCD. The operating current drawn by the artire circuit does not exceed 150 µA.

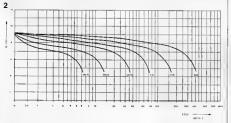


Figure 2, Typical cheracteristics of a 9 V elkelimenganess bettery. With a minimum bettery voltage of 6.2 V and average load resistance of 75 k(2, the operating time obtained is approximately 5400 hours!

by adjusting preset potentiometer P1 to obtain exactly 273 mV at INLO. The temperature sensor ICL 8073 draws a current of approximately 50  $\mu$ A.

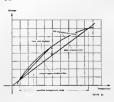
#### Energy consumption

A typical current consumption of I20 µA can be expected for the entire circuit. To calculate the life of a 9 V battery, the data sheet of the VARTA 4022 alkali-manganese battery was taken as a basis; this includes the typical discharge characteristics under continuous load using resistances of 100 Ω . . . 5 kΩ. These curves are shown in figure 2. The entire thermometer circuit operates quite happily with supply voltages of 12 V to approximately 6.2 V. Taking 6.2 V as the lower limit of battery voltage and an average load resistance of 75 k $\Omega$ , the group of curves can be extrapolated to obtain an operating time of approximately 5400 hours. This corresponds to continuous operation for more than 7 months. It is quite possible, however, that longer or shorter operating times will be achieved because the calculations relate to typical data for the discharge characteristic and current consumption of the temperature sensor and of the A/D converter. In general though, the typical data are met by approximately 50% of the components.

#### Precision

Linearity of the thermometer depends almost exclusively on the temperature sensor, because the specifications of the ICL7136 A/D converter are so good that its error contribution can be discounted. The ICL8073 temperature sensor can be procured in different classes of precision and specified temperature ranges. Its transfer characteristic has the form shown in figure 3a. The thermometer can be aligned at any temperature with preset potentiometer PI. The slope of the characteristic is needed to be able to calculate the measuring error in the region of this alignment point. In this case it is practical to replace the curve by a straight line as an approximation, as shown in figure 3a. After tha 25°C alignment the ideal characteristic is shifted as shown in

#### 3a



low-power digital thermometer elektor april 1983

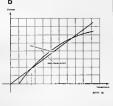
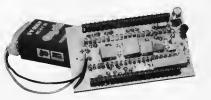


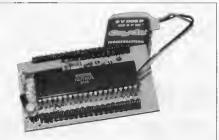
Figure 3. The linearity error of the thermometer in the region of room lemperature is best indicated by these characteristics. If, for example, elignment was performed at 25°C the resultant meximum error is ±0.2°C over a range of ±5°C around the slignmen point (figure 3b). Fraure 3e shows the real and ideal characteristics for the specified operating range. In the region of the alignment point, the cheracteristic curve is approximated by a

straight line.

figure Sb. If one calculates the error of the lowcost ICLBO'S JUTY sensor, whose specified maximum linearity error is ±1,5°C over the temperature range 0...7°C, the resultant maximum error is ±0.2°C over resultant maximum error is ±0.2°C over large of ±5°C cound the alignment point. The biphyrade ICLBO'S IUTY sensor would have a maximum error of ±0.0°C over larger would maximum error of ±0.0°C over larger would be aligned to the sensor of ±0.0°C over larger would be a sensor over l







low-power digital elektor epril 1983

.. and IC1 is mounted sbove them, as the second layer (using IC contact strips). The third leyer is the LCD displey, producine the final result on the first page of this articla.

Figura 4. Track pattern and component overlay of

the duel-cled, throughcontacting thermometer p.c.b.. It is constructed in three levels. Care should be taken to produce a strong mechanical construction, As an option, the sensor can be directly inserted into the p.c.b..

4







#### Parts list

Resistors (1/8 W): R1 R3 R6 = 580 k R2 = 180 k R4 = 82 k R5 = 22 k

P1 = 10 k preset (multiturn)

Cepecitors (see text): C1 = 47 p

C2 = 150 n C3 = 470 n C4 = 100 n

C5 = 33 n C6 = 2µ2/25 V tentelum

#### Semiconductors:

T1 = BS 170 or BC 549C (see circuit description) IC1 = ICL 7136 (Intersit) IC2 = ICL 8073 JIUT (Intersit)

Miscellannous: 9 V compact battery with terminels and leads 3%-digit liquid crystal display Hitachi LS 007CC H 1331C-C

LXD 4305R03 Hamlin 3901, 3902 Norsem NDP 530-035A S-RF-P1

converter by the sensor; its value should be exactly 100 mV. However, this error is mainly noticeable at the limits of the measuring range and is negligible in ambient temperature measurements.

#### Construction and alignment

Construction of the digital thermometer using the printed circuit board of figure 4 requires some care. The thermometer is constructed in three levels. Fit all passive components first. The capacitors within the socket for IC1 (IC contact strips) must be bent over to prevent them from protruding beyond the contacts. Incidentally, these capacitors must be fully encapsulated in plastic to prevent short-circuits. The contacts used for the liquid crystal

display can be of the same type as those for IC1. It is better, however, to use the more solid, plastic-base type.

The temperature sensor IC2 can be directly inserted into the p.c.b. or solder pins can he inserted in its place, to allow its connecting leads to be soldered on later. IC1 forms the second level of the ther

mometer module. The liquid crystal display is mounted on it in piggyback fashion. Care should be taken to produce a strong mechanical design, i.e. so that IC1 and the display are firmly seated in their sockets.

Since the thermometer is particularly suitable for measuring room temperature, on account of the sensor used, the 'comparison method' is utilised for simple alignment. In the Elektor laboratory a standard alcohol thermometer was used as a reference. Pl is adjusted to obtain its value on the LCD. An occasional comparison between the two readings indicates whether the low-power digital thermometer is accurate. It may be necessary to readjust Pl. Incidentally, there is no on/off switch in our circuit. With a current consumption of 150 uA such a switch seemed en unnecessary luxury!

MC/MM phono preamp elektor april 1983 The phono preamp is an important part of the Prelude, or indeed of any audio system. Gramophone records are still the recording medium that offers the highest quality, provided a good cartridge and preamp are used. Most of the really good cartridges are the moving coil type, and these require a step-up transformer or, as is more common nowadays, a so called pre-preamp. The phono preamp and the moving coil pre-preamp described in this article are designed to form a single module. This can be incorporeted in any audio system, although it is intended as part of the Prelude.

# MC/MM phono preamp

100 μV in, 100 mV out



Photo. The complete MC/MM phono preamp module. Once all the connecting wires are in position, it is a fairly rigid construction.

> A phono preamp must do two jobs. It must boost the output from a moving magnet (or 'dynamic') cartridge to a sufficient level, and it must modify the frequency characteristic in a precisely-defined way. Figure 1 shows the theoretical recording characteristic as a set of bold straight lines, running from lower left to upper right; the thin line running through these is the actual characteristic. To achieve a flat overall response, the inverse characteristic must be applied during playback; this is the thin line running from upper left to lower right in figure 1. Achieving this type of characteristic may seem quite a feat, but in fact it is mainly a question of ensuring that a set of normalised RC time constants are included in the preamp. For once, the Americans agree with us: their RIAA response corresponds to the

European IEC specification. Reading from left to right in figure 1, the first time constant is  $3180~\mu s$ ; this causes the response to fall at 6 dB/octave from about 50 Hz. Then we come to  $318.\mu s$  — this tends to flatten out the response above 500~Hz — almost immediately  $500\,\text{lowed}$  by  $75\,\mu s$ , which leads to a further 6 dB/octave slope above 2120~Hz.

Knowing what time constants are required is one thing; knowing where to insert them in the circuit is another. There are two basic possibilities; you can either use passive RC networks, or you can include them in a feedback loop to obtain active filters. Obviously, since there are three time constants to be included; you can also include one or two as passive networks and the rest as an active network. Some of the possi-

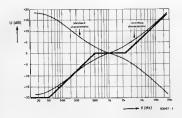


Figure 1. The frequency characteristic used when cutting a record (lower left to upper right), and the response required during reproduction (upper left to lower right).

bilities are illustrated in figure 2.

A passive filter can be mounted at tha preamp input, as shown in figure 2a. This has the disadvantage that the signal level is drastically reduced at high frequencies (—50 dB or soi) before it reaches the input stage. This is asking for a poor signal-to-noise ratio.

So, try it the other way: mount the RC networks at the output, as shown in figure 2b. Now we run into a new problem: the input signal is at such a level that it will not only mask the noise, but there is a good chance that it will drive the preamp into distortion! The most common approach is shown in figure 2c. The RC networks are included in the feedback loop. Provided the circuit is properly designed, this can give quite good results. However, the system shown in figure 2d is even better: the two lower time constants are included in the feedback loop, so that there is little risk of overloading the preamp. The third RC network. however, is included as a passive filter at the output. This means that the higher frequencies are passed through the preamp at relatively higher levels, thus improving the overall signal to noise ratio. As an added bonus, this system makes it slightly easier to design a good preamp: rolling off the frequency response of an amplifier towards higher frequencies tends to lead to instability!

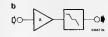
Reading between the lines in the last paragraph, it will be obvious that the circuit described here corresponds to figure 2d.

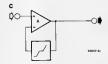
#### The moving magnet preamp

The right hand section of figure 3 should by move be familiar basically, it is the 'discrete opamp' that is used throughout the Prelude. The circuit was discussed at length in the articles on the headphors amplifier and the ine amplifier, so there is little point in repeating the whole story here.

The point is the point in the point in the commonder of the point in the commonder of the point in the commonder amitter line (T3). The collector output currents are combined by







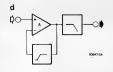


Figure 2. The official tim constants can be included in the preamp in several ways. Purely passive networks can be used, either preceding (2e) or following (2b) the preemp; or the networks can be included in the feedback loop (2c). The solution shown in figure 2d (two time constants in the feedback loop and one as a passive network at the output) gets the best of both worlds.

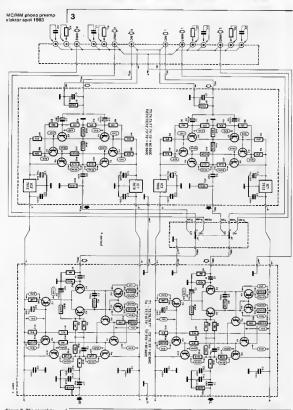


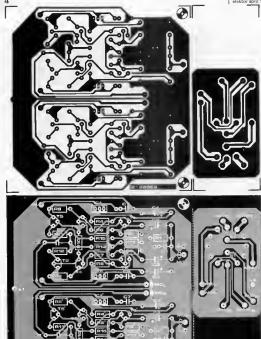
Figure 3. The complete circuit, with moving coil pre-preamp and moving magnet presamp. Additional input sockets are provided, so that the input impedance can be tellored to suit any particular certridge.

Parts list for MC pre-

Resistors: R1,R1' = 120  $\Omega$ 

R2...R5, R2'...R5' = 1k5 R6,R6',R7,R7' = 15 k R8,R8',R9,R9' = 1k8 R10,R10' = 820  $\Omega$ R11,R11',R12, R12' = 27  $\Omega$ R13,R13' =  $8\Omega$ 2 R14,R14' = 150  $\Omega$ R15,R15' = 100 k





Capacitors: C1,C1° = 120 p C2,C2°,C3,C3° = 220 µ/4 V C4,C4°,C5,C5° = 10 µ/35 V C6,C6° = 242 C7,C7°,C8,C8° = 330 n Semiconductors: T1,T1\*,T4,T4\*,T5, T5\* = BC 550C T2,T2\*,T3,T3\*,T6, T6\* = BC 560C IC1,IC1\* = 7812 (5%) IC2,IC2\* = 7912 (5%) Miscellaneous:

S1 = 2-pole, 3-way p.c.board mounting rotary switch extension spindle for

extension spindle for \$1, with front-panel mounting bearing Figure 4. This board actually consists of two perts that must be separated. This larger section is for the moving coil pre-preamp; the smaller piace is for mounting the input selector switch,

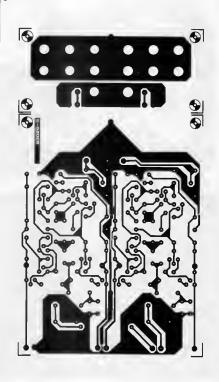
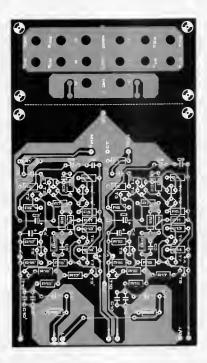


Figure 5. The p.c. board for the moving magnet preamp and that for the cinch input sockets. These boards must be separated before mounting any components.

means of a current mirror (T4, T5) and passed via a Darlington stage (T6, T7) with a current source as collector load impedance (T8) to the class A output stage (T9, T10). The feedback loop (C5, R7 ... R10 and C3) includes the first two time constants, as

explained above. This means that the response is flat above about 500 Hz, with a gain of 50 set by the ratio of R8/R9 to R7. From 500 Hz down to 50 Hz, the response rises at 6 dB/octave; below 50 Hz, the gain is again constant (500, set by the ratio of



#### Parts list MM preamp

Resistors: R1,R1' = 1 M R2,R2' = 120 k R3,R3' = 100  $\Omega$  R4,R4',R5,R5' = 270  $\Omega$  R8,R6',R13,R13' = 4k7 R7,R7' = 180  $\Omega$ 

R7,87' = 180 Ω R8,85',89,89' = 18 k R10,R10' = 82 k R11,R11' = 33 k R12,R12',R15,R15' = 1k8 R14,R14',R16', R16' = 820 Ω R17,R17',R18', R18' = 47 Ω

R17,R18, R18\*= 47 Ω R19,R19\*= 1 k R20,R20\*= 330 k R1<sub>X</sub>,R1<sub>X</sub>\*,R2<sub>X</sub>, R2<sub>X</sub>\*= see text

C3,C3\*= 220 µ/6 V C4,C4\*= 10 µ/16 V C5,C5\*= 39 n C6,C6\*= 68 n C7,C7\*= 2µ2 C8,C8\*,C9,C9\*= 100 n C10,C10\*= 8n8 C1x,C1x\*,C2x, C2x\*= see taxt

C1,C1\*= 1 µ C2,C2\*= 100 p

Semiconductors: T1,T1\*,T2,T2',T3,T3', T8,T8',T9,T9' = BC 550C

T4,T4',T5,T6',T6,T6',T7, T7',T10,T10' = BC560C

Miscellaneous\*

14 metal cinch sockets, screw-mounting
8 cinch plugs for R<sub>X</sub> and C<sub>X</sub>

R8//R9 + R10 to R7). The third time constant, causing the response to roll off above 2120 Hz, is the passive output network (R19, C6 and C10).

There are three inputs, selected by means of S1: the moving coil pre-amp and two moving magnet inputs. These are shown at the left in figure S. There are actually three sockets for each of these inputs: one for the signal input, and two for impedance matching ( $R_X$  and  $C_X$ ). This is explained in detail in a separate article ("RC equalizer"), elsewhere

MC/MM phono preamp elektor epril 1983 in this issue. Note that the basic input impedance of the preamp is 107 k (R1//R2), so that four 82 k resistors must be inserted in the  $R_X$  positions to obtain the 'standard' 47 k input impedance.

#### The moving coil preamp

Moving coil cartridges give a beautifully 'clean' signal, but at e very low level (100...500 µV). This means thet a 'pre-preamp' must be included between the cartridge and the 'normal' moving magnet

will give 100 mV dut.

The DC setting is determined by R2, R3, R6 and R7; the current through the input devices is determined by R4 and R5. This means that anyone who feels like experimenting with different input transistors can easily set the optimum collector current by mod-

ifying these two resistors.

The positive and negative sa

The positive and negative supplies are derived from the main +15 and -15 V rails. Integrated 12 V regulators are included, mainly to ensure that no hum, noise, interference spikes or whatever can possibly reach the pre-preamp.

The input impedance is approximately  $100 \Omega$ : suitable for prectically all moving coil cartridges. If an even lower impedance is desired, the value of R1 (and R1') should be reduced accordingly.

#### Construction

Although the moving magnet preamp and the moving old pre-preamp are both complete units that can be used separately, we will only discuss the construction of the complete phono input module — using both, in other words. Even if the pre-preamp is not (yet) needed, it is best to use both boards to bothain a relable electrical and mechanical construction. No components need be mounted on the second board in that

As can be seen in figures 4 and 5, both boards consist of two sections. Before doing anything else, these must be separated. The small piece from the moving magnet board is intended for cinch-type input sockets; the piece that is cut off the other board is used to mount the mput selector switch. As usual, good quality components should be used, 187, 188, 197, and for board to the complete, the complete, the cut of the other discountered at right-angles at one end of the moving-oul board (on the component side or complete, the code with the input sockets is mounted at right-angles at one end of the

near the electrolytics) with the connections MCL, 1, MCR and 1 mating correctly. These four connections run from the track side of the cinch board to the component side of the moving coil board.

The next step is to mount a set of connection witer. The four sets of three einch plug must be interescentler, and a longish lead is soldered to the seather, and a longish lead is soldered to the seather, and soldered to the seather, and soldered to connection to the track side). Four wires (4 or 5 cm long) are soldered to connections MMIL, MMIR, MMZL and MMZR on the selector switch board, again basking back from the copper track side. Two otherest section (2 cm) or so) are connected to MCL and another pair to points MML and MMR on the opposite eight.

Having done all this groundwork, it is a good idea to compare all the bits with the photo of our prototype. When the MCL and MCR leeds from the selector switch board are mated to the corresponding points on the moving-coil board, the four long leads from the switch board should reach easily to the cinch connectors. Note that the indications near the leeds correspond to the four sockets that they must connect to. These leads can now be shortened to length and soldered to the sockets. Next, the moving magnet board can be mounted (component side facing in); this involves two leads from the selector switch board and one from the input sockets. Finally, five interconnections are made between the two preamp boards - note that

tween the two preamp boards — note that they all run straight across. The completed modile on now be mounted on the Pfetule bus board. Unless you are not the Pfetule bus board. Unless you are set to be a support of the present of the present of the present of the present of the modules are also in position, including the tone control unit described this month, you can proceed to the first test. With either of the Min inputs selected, no noise should be audible; for the Min input, and that has may be audible with the present of the present o

#### Input impedance matching

To get the best out of a dynamic (moving magnet) cartridge, the input impedence of the preamp must be properly matched. This is dealt with in greater detail elsewhere in this issue, but a few points are worth noting. The input impedance of this preamp is approximately 107 k, parallel to 25 pF. As a first approximation, es mentioned earlier, four 82 k resistors can be mounted in the Rx sockets. This brings the impedance down to 47 k. In prectice, most cartridges tend to give best results when loaded with approximately 300 . . . 500 pF; bearing in mind that the cable capacitance can be anything from 50 pF up to a few hundred pF, it is worth experimenting with several values for C-(from zero up to about 470 p), to see which gives the best results. So much for the analogue section of the

So much for the analogue section of the Prelude. All that remains for the next few issues are some final comments, practical tips, end the ramote control unit.





## National filter with application fault

We are grateful to an observant reader for pointing out that the 'Applikator' in the Septembar 1982 issue, concerning the National Semiconductor MF 10 filter module, contains a pretty silly mistake. In figure 5, a Buttarworth 4th order fow-

In figure 6, a Buttarworth 4th order fowpess filter with a cutoff frequency of 2 kHz was essemblad by connecting two 2nd order filters in series; each of these two filters also had a cutoff frequency of 2 kHz.

Of course, that simply will not work!

When connecting filters in series, both the Q and the cutoff frequency of each individuel filter must be determined, in accordance with the filter type. The correct filter data are specified in the figure.



In order to mark the fronts of dispars whiches the have already been soldered in, there are two possibilities: the switch in the moving upper part must be assumed from the moving upper part must be assumed from the flower soldered part of the displars watch. The scool possibility cere be amplificate watch. The scool possibility cere be amplificate watch. The supper part the watch first the switch first belowed and then giantly upwerful. The upper part then jumps out. To insert it again, only a slight pressure from above its required to engage the upper part once more.



## Slave flashgun powered by the

There is a requirement for a slave flushigum for taking is sense of pictures indoors without the isonomeniance of rechargable or disposable batteries. Provering the result from the mains would enable it to be used from the mains would enable it to be used in the result of the resul

course he is right. The only problem is that particular attention must be paid to efectrical safety because of the lack of isolation from the meins. It did not take long to design the circuit. We have not vet tested it, but the circuit has been assembled from well-proven circuitry including the "Sleve flash trigger" described in the 1982 Summer Circuits issue. The ffesh tube end ignition transformer are available by mail order and from some electronic component shops. The sensitivity greatly depends on the size of solar cells SZ1 and SZ2. When switch \$1 is closed the flesh energy is doubled. The 100 ohm resistor in the lead limits the charging current to values which are sultable for electrolytic capacitors and the maine

#### Crescendo coils and S/N ratio

Some questions were reised regarding winding of the 2 x 10 turns of coil L1 in the Crescendo power amplifier. This is clerified in the drawing.

Reeders have also pointed out that the signal-to-noise ratio is missing from the Technical Detai. There is not much that can be measured; with no signal applied to the input, the output is absolutely quet. Expressed in decibels, the figure is SNR greater than 110 dB, referred to reted power.



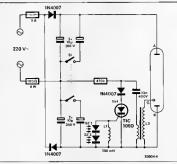


The result is that the simple credit of regura 7 cannot be used. In order to achieve different cutoff frequences that we fill the two filter sections, at least one of them must be constructed according to figure 4; have it be cutoff frequency not only depends on the clock frequency but also on the ratio RZ-RA. Another possibility is to apply two different clock graphs (corresponding to the demined filter graphs (corresponding to the demined filter puts CLRs and CLRs in the circuit of light PX. Another possibility and CLRs in the circuit of light PX. Another possibility is to apply two different clock in the control of the control of light PX. Another possibility and CLRs in the circuit of light PX. Another possibility and CLRs in the circuit of light PX.

### Marking of digitast switches

Hare is a tip from a reader.

If the tops of digitast switches are marked



membrane switches elektor april 1983 The membrane switch — or foil switch as it is becoming popularly known — is almost too good to be true. The switch is both reliable and inexpensive. These two factors coupled with its 'scienca fiction' appearance can provide the basis for a very elegant and economical keyboard that the more conventional switch cannot hope te match.

# membrane switches

the super-switch of tomorrow? If Asimov had been a hardware designer insteed of a science fittion writer we would have hed the membrane switch about twenty years ago! They really do have the aura of space-age technology. The most striking optin about them is their appearance, they are, quite literally, a ware thin. This fact, pared oxidally, it a disadvantage from the pared oxidally, a disadvantage from the power of the striking of the stri

From the aestbetic point of view, a keyboard consisting of membrane switches has no equal. They can be any colour you like (including black), are dust proof, rust proof and would probably suffer no harm if they went through the washing machine a couple of times!

How is this list remarkable of edvantages achieved? The illustration in figure 1 will help to answer many of the questions. The 'exploded diagram' shows the total absence of 'working parts'. No springs, rockers, gold plated sliding contacts, in fact, not even a terminal Each switch consists of four layers of plastic foil that are common to all the switches in the keyboard.

The top layer of plastic is the actual keyboard panel, the part that is touched when the switch is operated. This can be coloured by a special printing process and can take any design or pattern that is desired. Further, the keyboard itself can be any shape that comes to mind, including, in some cases, bending round corners. We would like to see

the toggle switch that does that! The switch contacts are carried on two layers of flexible plastic and these are separated by a third layer of polythene foil. This 'spacer' has holes or cutouts in it that coincide with the switch contact areas in the upper and lower foils. When the front panel, or 'keyboard', is pressed, the upper contact layer us locally distorted allowing the two contact areas to 'make'. The contact areas are made from silver, graphite or a mixture of both. The insulating layer or spacer is only a few thousandths of an inch thick and therefore operating pressure is very low. This creates the misconception that the membrane switch is a true 'touch' switch, that is, a capacitively coupled solid state switch. As

The complete keyboard is connected to the outside world via a flexible connecting stip. This normally pushes into an edge connector a printed circuit board but it can also be soldered directly onto a board if needed. However, soldering such a thin (plastic!) strip is fraught with much difficulty and is not encouraged.

we have seen, this is not the case.

#### Advantages

The manufacture of a keyboard consisting of membrane switches is very easy. It is also economical due to that fact that a number of keyboards can be printed on a single sheet and then cut out.

The operation of a switch is simple, a soft touch is all that is required. If necessary a

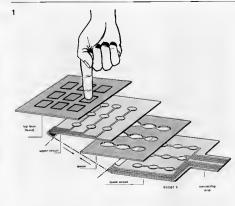


Figure 1. Membrane switches consist of four layers of plastic foil. The secret of the switch lies in the holes in the spacer. This is where contact is made between the upper and lower foils when a keypad is pressed.

light can be fitted behind the switch to show that it has been operated as anybody who has ordered a 'Big Mac' can recall.

The complete keyboard is entirely water and dust proof. Corrosion is also not a problem as very few chemical substances can attack the plastic foil used.

Mounting the keyboard is simplicity itself it is usally backed with e self stick material and the whole keyboard is just placed on a sheet of aluminium and that is that!

As mentioned previously, shape can take many forms for any number of applications. This keyboard has no fear of that horror of horrors – the office coffice. Good key-boards have been known to die instantly from a liberal does of this but the following the contract our test on quite happily. A good many the contract of the contract o

Cost always reers its ugly head whan anything good comes along but in this case there is a heppy ending. The complete keyboard can be produced at e fraction of the cost of conventional keyboards.

What ebout long term reliability? With no moving parts what can go wrong? A persistently strong finger may make some impression after e hundred years or so but it is unlikale!

So we now have what appears to be the super switch of the century. Does it have no Achillas heal?

#### Disadvantages

If one is ill-advised enough to attempt to switch 10 amp loads with our switch it reveals a marked tendency to get somewhat 'sopy' - in a very few microsconds' (It also creates severe bluntness of the fingers.) in all fairness, the restriction to 100 mA loads can not really be classed as a disadvantage es that is more than enough to carry out the purpose for which it was intended. Whether high current versions will eventually be produced in yet to be seen.

be produced is yet to be seen. Switch configuration looms large on the disadvantage side of the fence. We have a choice – press to make – or nothing! Not even e single pole changeover. Here again, for their alloted task in life, that of a key-board switch, this is sufficient.

#### Where to use membrane switches

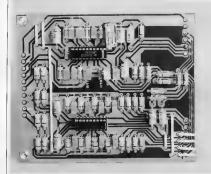
As we heve seen, the membrane switch will not make the conventional switch in its many varieties instantly redundant. It is also not really economical where just one switch is required. However, when keyboards ere considered it leaves the competition gasping for breath, especially when cost is considered.

The Elektor 7-day multi-time switch that is published in this issue makes use of e membrane keypad. It is available from the Elektor ESS service.

#### Technical details

thickness approximately 1 mm perating pressure: 2 N approximately 0.2 mm load current:  $00 \text{ m} \wedge 43 \text{ OV}$  contact resistance.  $400 \Omega \times 30 \text{ V}$  contact resistance.  $400 \Omega \times 30 \text{ V}$  and 1 ms 2 ms 2

contact operational temperature:-30°C to +65°C



The convenience of remote-control for television sets is fairly well-known. The Interlude brings the same armchair operation to the world of hi-fi audio: volume, balance, tone, even input select — all controlled from the 'ideal listening position'.

Although this unit is intended as a plug-in module for the Prelude, it is almost a complete preamplifier in its own right. You only need a little ingenuity, a few hard-wired potentiometers and a power supply?

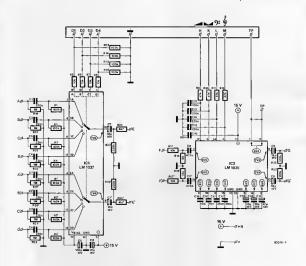
# interlude

a remotecontrol preamplifier To provide a preamplifier with a remotecontrol facility, the first step is to ensure control facility, the first step is to ensure means of a DC voltage. The input selector switch, for example, could be replaced by a box of relays or by so-called analogue switches. However, relays are expensive and require heavy duty drive circuits; conceptive heavy duty drive circuits; conbut they are not really good enough for a top-notch acid to system.

The analogue controls (volume, balance and tone) are even more difficult. An isteal solution would be to use some kind of motor-drive for the potentiometers: just think of the gimmick-value of a knob that is rotated by a ghostly hand! Yes, and just think of the (mechanical) problems, another alternative would be to use O'Tas (Operational Transconducture Amplifiers) as electronic potentiometers. Unfortunately, To out a long story short: any type of remote-control system is an audio installation

will have some disadvantage. Either it will be exbe mechanically clumry, or it will be expensive, or both; and if it is not too expensive, the quality will almost certainly be less than that offered by a more conventional preemplifier design. The solution chosen for the Prelude gets the best of both words; in a surprising way use two preamplifiers! The Prelude itself is a topquality design, using conventional controls. When switched from 'manual' to 'remote', it control section is trud tot of circuit and replaced by a remote-controllable preamp: the Interfule.

This circuit configuration made the name 'Interduce' an obvious choice. Similarly, the infeared transmitter and receiver (to be described in forthcoming issues) are called the 'Mastro' and 'Conductor'. for an obvious reason: how size would you call someone who controls a musical performance from a distance? Not 'recording engineer', 'commissier' or 'producer', surely!



#### Second best?

As mantioned abova, activating the Interluda means that the quality must suffer. This is the price of convenience. However, the overall specifications are not at all bad, as shown in table 1. The Interlude may not be XLent, but it cartainly is hi-fi. For just two ICs, as shown in figure 1, that's not bad. These two, the Netional Semiconductor LM 1037 and LM 1035, are in a friendly environment of course: the input selector, IC1, only needs to select inputs of adequate level (approximately 100 mVRMS). These are provided by the tuner, tape deck or whatever, or by the existing phono preamps in the Prelude. The control amplifier, IC2, needs e fairly high signal level (1 VRMS or so) and this is provided by the existing lina amplifier.

#### The circuit

IC1 is the equivalent of a double pole switch with four positions. Dapending on the logic

#### Tabla 1

Figure 1. The circuit of the Interlude merely consists of two ICs.

Distortion factor (st 1 kHz, 1 V<sub>rms</sub> output): Frequency range (+0, -1 dB): Signal-to-noise ratio tuner, aux, tape: MM1, MM2: MC:

Typical technical data

> 75 dB > 65 dB > 55 dB Tone controls: bass (40 Hz): 2 15 dB treble (16 kHz): ± 15 dB

Cross-talk (20 Hz . . . 20 kHz): > 40 dB Voluma control, range: 80 dB

Balanca control, attenuation of 1 channel:

20 Hz . .. 20 kHz

+1 dB ... -26 dB

< 0.15%

interlude elektor april 1983

Figure 2. Cable instead of

an inexpensive way of testing the Interlude, it

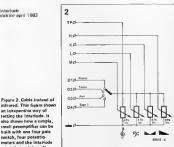
elso shows how a simple,

small preemplifier can be

buils with one four pole

meters and the interlude printed circuit board!

switch, four potentio-



levels at control inputs D1 . . , D4, the corresponding signal inputs A . . . D (A' . . . D') are switched to output E (E'). In order to select an input, a voltage of 2.5 V . . . 50 V must be applied to the corresponding control input. At a control voltage of less than 1.0 V the corresponding input is inhibited. Resistors R1 . . . R4' provide the bias voltage for the IC inputs and also determine the input impedance (in conjunction with the potentiometers on the connecting board). Outputs E and E' are low-impedance. The

gain of IC1 is 0 dB (unity gain). IC2 contains six electronic potentiometers; volume, treble and bass require one potentiometer each, times two for stereo. Balance control is achieved by varying the volume potentiometer. The latter also has the widest adjustment range: more than 80 dB. The treble and bass controls give symmetrical cut and boost: ± 15 dB at 16 kHz and 40 Hz, respectively.

The input signal for IC2 comes from the

3

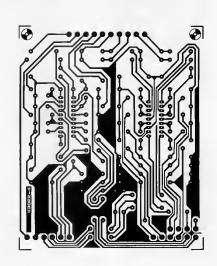


Figure 3. Terminals +B (15 V), A . . . D', E and E', F and F', G and G' and 1 are wired so that the printed circuit board can be 'plugged' into the bus board of the Prelude

line amplifier of the Prelude, which delivers approximately 2 V<sub>rms</sub>. This signal (at points F and F') is reduced to the optimum input level of 1 V<sub>rms</sub> for the lC by means of R17/R18 and R17'/R18'.

The control voltage for the potentiometers at terminals H . . . M can vary between 0 V and the voltage at point TP (5.4 V). A load of up to 5 mA can be connected to point

TP (potentiometers, for example). The IC offers an additional feature which is not exploited in the Prelude: a loudness facility. In order to make use of this optional function, pin 7 must be connected to pin 12 instead of pin 17.

#### Construction

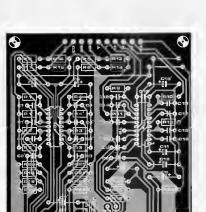
The printed circuit board is mounted on the bus board in much the same way as all the other boards in the Prelude. The connecting wires are fitted to points +B (15 V) A... D', E and E', F and F', G and G' and

1. As usual, the short lengths or wire (about 2 cms) are soldered in place and then bent parallel to the board. This very economical 'edge connector' is fitted and soldered to the bus board in the position shown in figure 3 on page 2-20 of the February 1983 issue. The printed circuit board must be fit with the component side facing to the left when

looking at the front of the Prelude.

Sockets for the remote control connections can be mounted on the rear panel of the case just behind the printed circuit board. Either one ten pole socket can be used or, perhaps more economically, two five pole sockets. These latter can be ordinary DIN sockets but, to avoid confusion, they should be of different configurations (for instance, one 180° and the other 270°).

The Prelude is now ready for remote control. The circuits for the infrared transmitter and receiver will be published next month, Meanwhile, the circuit shown in figure 2 can be used for testing.



#### Parts list

interlude

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Resistors. R1... R4. R1'... R4' = 100 k R5 . . . R8,R17,R17' = 10 k R9 . . . R12 = 47 k R13 . . . R16,R25,R25' = 100 % R18,R18' = 18 k R19 . . . R22, R19' . . . R22' = 1 M R23,R23' = 330 k R24,R24' = 4k7

#### Cepscitors:

G1 . . . G4, G1' . . . G4', C12 . . . C16 = 220 n C5 = 100 µ/10 V C6,C7,C7',C9,C9',C18, C18' = 10 µ/16 V C8,C8' = 470 n C10 = 100 n C11 = 47 µ/10 V C16,C16' = 10 n C17,C17" = 390 n

#### Semiconductors: IC1 = LM 1037 (National Semiconductor) IC2 = LM 1035 (National Samironductori

frequency response equalisation for moving magnet cartridges



RC equalizer

terminated with the proper impedance in order to achieve optimum sound. This article attempts to clarify the electro-acoustic factors involved and presents a simple and inexpensive method of obtaining a great improvement in sound response.

Anyone who has purchased a record player or amplifier after extensive listening tests in a hi-fi studio will have noticed two things:

1. Each record player and each amplifier

sounds different.

2. The new piece of equipment sounds different at home than in the shop.

different at home than in the shop. One should not be tempted into resignation in the face of these diosyncracies of eudio-electronics. These differences in sound have a perfectly rational explanation and can be measured. The only question is how.

Townships Cold Statements Stateme

#### Cause and effect

The causes of the differences in sound can be found in the construction of a moving magnet system, and are explained by the equivalent circuit of this type of pick-up in figure 1. The stylus is connected to a small permanent magnet, Colls are arranged in its magnetic field and the field variations caused by stylus movement are converted to a varying voltage by these colls.

varying vottage by these coils. This type of coil has many turns. Since there is very little space in the cartridge, the wire used for the windings is very thin. In addition to the coil inductance (L: 200 mH... 1 H), this leads to e considerable internal resistance

this leads to a considerable internal resistance (Ri: 200  $\Omega$  . . . 1000  $\Omega$ ) and capacitance (Ci: coil and cable, up to 100 pF).

Carridges are designed to exhibit a flat frequency response when terminated with a particular impedance. In conjunction with the terminating impedance, the influence of L, R; and C; is neutrelised. The DIN standard specifies a terminating impedance of 47 k/400 p.

Everything would therefore seem to be in order as long as  $C_L + C_X + C_D$  result in 400 pF, and  $R_X$  and  $R_D$  in parallel have a value of 47 k $\Omega$ .

Figure 1. The electrical equivalent circuit of a moving magnet certridge, connected to the preemplifier by a cable. But things are seldom so clear-cut, Manufacturers of cartridges and hi-fi equipment tend to interpret this standard as 'merely a guide'. Cartridges are actuelly designed for terminating impedances of  $33~\mathrm{k}\Omega$ ...  $100~\mathrm{k}\Omega$  end for terminating cepacitances of  $80~\mathrm{pF}$ .

. In F. end readers who follow the tests in hi-fi magazines will have noticed that the input impedances of preamplifiers for megnetic pick-ups oftan deviate considerably from the vehice specified. Only so-called highend equipment is provided with a means of selecting the input impedance.

It is therefore no wonder that different performance is encountered when record players end amplifiers are combined in e system.

#### Phono-equalizer

Since we cannot change the cable cepacitance Ct, anymore than we can change the construction of the pick-up system, the only solution is to match the preamplifier. Within certain limits, this is possible without modifying the unit, using a so-called phonoequalizer. This accessory is a box which is inserted in the line to the pick-up (or 'phono') input of the amplifier. Various capacitors and resistors can be switched in parallel with the input by means of pushbuttons or other switches. In view of the very low cost of the components used in this accessory, it is clear thet one is mainly paying for the technically sounding name 'phono equalizer'. Furthermore, there is no quarantee that this accessory will result in any improvement.

According to the usual operating instructions, a record with plenty of overcones should be played and different settings should be selected on the phono-equalizer, until the sound is right. In many cases, however, the result is merely a difference in sound and not a technical improvement. At any rete, it is not e solution worthy of the name 'hife.'

#### RC equalizer

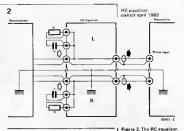
A better solution is to use an RC equalizer. As shown in figure 2, this accessory for home-construction simply consists of e metal box with two input sockets, two out-

put sockets and four extre sockets.

The input and output sockets are interconnected; the box is tharefore inserted in tha line from the record player to the amplifier like a phono-equalizer. The extra sockets ere intended to accept Cinch plugs containing a small capacitor or resistor.

In this way we have the equivalent of a phono-equalizer, i.e. a facility to connect capacitors and resistors in parallel with the input of the amplifier, but at much lower cost and with more flexibility.

The sockets must be insulated from the metal case; this is important to avoid hum. The easiest way to achieve this it to use plastic weshers of a suiteble diameter. The metel case then has an earth from the turn-table, which is usually separete on most record players, and another terminal for the earth conductor to the amplifier.



Incidentally, on RC equalizer of this type is already provided in the latest Elektor preamplifier – the Prelude.

#### Equalising

We now have a box with which we can connect espacinos end resistors so desired. So far so good. But what values do we need? We could consult the manufacturer's literature to esteblish what load impedance is required by the cartridge end what input impedance is provided by the preamplifier. Alternatively, we can use e test record. This should include a sineware sweep from 50 Hz to 20 kHz or so.

Measurements can be done by ear, with the aid of the test circuit of figure 3 and a conventional, analogue multimeter. With the RC equalizer in circuit, we can now set to work.

 Set the tone controls to their midpoints or, even better, switch them off if possible; cancel all filters (subsonic, loudness, etc.).

 Play the record with the sweep, and edjust the vohime record to obtain e readable deflection on the multimeter (test circuit connected to the loudspeaker output).

 If the deflection remains constant at high frequencies (± 15% of the value for low frequencies is ecceptable), the system is in order and no RC equalizer is needed.

If, however, the deflection clearly changes es the frequency rises, continue with the actual alignment: starting with smell values for R (16 ft...), and high values for R (16 ft...), solder capscition and results of the starting of the starting of the same starting that the

If the frequency response cen only be worsened with the RC equalizer, the input impedance of the preamplifier must be too low elreedy. This means modifying the input circuit, but this is best not attempted without e circuit diagram. Since it only consists of a box with six Crich sockets and four additional pluss, it is considerably lass expensive than a commercially evallable phono-equalizar.



Figure 3. This test circuit allows even the highest frequencies to be measured with accuracy. It is connected in perallal with the loudspeakers.

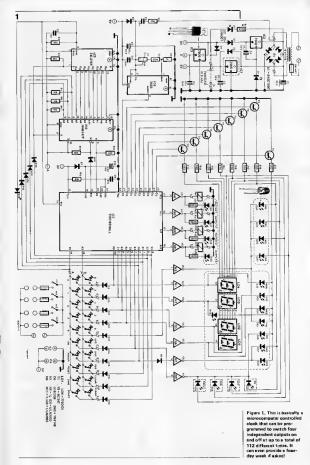
# 7-day timer/controller



a micro computer controlled time switch The TMS 1601 timer/controller IC from Texas Instruments forms the basis of this incruit and, as can be expected, carries out most of the work. So what does it actually do? Briefly, it is a single-chip reprogrammed microprocessor dedicated to time sense segment LED displays and provides more proposed to the control of th

tively, four switching times for each output can be set to repeat daily. In total, this means enough switching times per week to take care of Coronation Street and Dallas and egg for breakfast every morning and still open and shut the garage door morning and night (weekdays only) without even struggling! Oh, and don't forget the porch light!

Contrary to what you might expect, programming this unit is a fairly simple matter. For example, say that the porch light needs to be switched on every saturday evening for a certain period of time. Using the



7-day timer/controller Elektor April 1983

Missing link?

For complete flexibility, it

'weekly' and 'daily' times

Unfortunately this is not

manufacturer's specifi-

We have been informed

that there is indeed an

error in the programming mesk for the chip. TI is

rejuctent to correct this.

which seems rather a

pity -

possible - contrery to the

cetions - as we discovered

in one switching sequence.

should be possible to

program a mixture of

keyboard, key in the day of the week followed by the switch-on time and then the switch-off time. That's all there is to it! Alternatively, if the porch light needs to be switched on every evening, as it probably will, simply press the 'DAILY'

All switching times that are stored in memory can be displayed whenever required. Each or all of them can be deleted or modified easily. There is also a 'reset' facility that may be of interest to people who have an application involving repeated 24 hour cycles, but more of that later

One other mode of operation exists that can be very useful. It is possible to switch any output manually at any time without affecting the program stored in the memory. This has the added advantage that the timer can also be used as a central control point for all the appliances that are connected to it. Appliances can be connected to the timer via relays or solid state switches. These in turn can be mounted in the appliance itself or in the same housing as the timer. This allows the possibility of driving equipment using only low voltage wiring, a useful asset in a number of situations.

A final detail that is not the least in importance. The circuit bas been equipped with emergency battery backup. This ensures when testing our prototype. that in the event of a mains failure the clock continues to run and the memory

remains intact.

#### The circuit diagram

About a dozen or so years ago, the detailed description of the circuit diagram for this project would probably have required a small book! Fortunately this is not the case today as a glance at the circuit diagrem of figure 1 will show. The beart of the circuit is IC7, the TMS 1601. This IC is a dedicated timer/controller microprocessor and we would not get anywhere without it (in this circuit at least). It contains an internal clock oscillator, 4 K byte ROM, 512 bit RAM and the four-digit sevensegment display decoder and multiplexer among other bits and pieces. Quite a tally for one IC

The keyed in information is stored in an external RAM, IC6. Three of the address lines for this memory are driven directly by IC7 while the others are controlled by a shift register, IC5. In its turn IC5 gets its information from output R9 of IC7, clocked in by a signal derived from output

R11 (IC7).

The output control relays (or solid state switches if preferred) are switched via buffers (N8 . . . N11) from the R12 . . . R15 outputs of IC7. An important point to note here, the quiescent current of the output devices must not exceed 80 mA. The LEDs D19...D22 indicate the state of each output,

The membrane keyboard is connected between outputs R0...R9 and KI and K2 of IC7. The key functions will be described later. The four seven-segment displays are multiplexed by IC7. The digit select outputs are R0...R3 and these

are buffered by display drivers N1 . . . N4 in IC9. Segment control is from outputs 00 . . . 07 and here transistors T1 to T8 are the buffers. The remaining display outputs R4 . . . R9 take care of the rest of the LED indicators (16 in all). It will be as well to list what these are. LD1 to LD4 are of course the seven-segment displays. These are accompanied by: a decimal point (D44), the days of the week (D32 . . . D38), the memory input LEDs (D24 . . . D27), an LED for the 'reset' (D28) and 'period' keys (D29) and finally the LEDs for the 'on' and off' keys (D31 and D30 respectively). The clock signal for IC7 is devired from the 50 Hz mains frequency and taken from the secondary winding of the transformer. The weveform at this point is used to synchronise the 7555 (IC4) which is connected as an astable multivibrator with a frequency of - 50 Hz! This apparent 'over-engineering' not only gives us a good square wave for the clock signal but it also serves as a clock oscillator if the mains supply fails. The frequency setting components in this case are resistor R2 and capacitor C5. This leads the story on to the power supply (emergency and otherwise). Two voltage levels are required for the circuit, -5 V and -9 V. Both are achieved by the use of voltage regulator ICs. The -9 V is the province of IC2; the -5 V supply consists of two sections, the supply for the display (IC1) and the rest of the

circuit (IC3). In the event of a mains failure (when panic sets in round the freezer!) the 7 NiCad cells take over - but not quite everything. The -9 V for IC4 and IC5 together with the -5 V for ICs 5 and 6 are maintained. This ensures that the clock continues to run and the memory remains intact. The display, with its relatively heavy current consumption, must go and so we lose that. The correct time is of course retained, it just means that it is no longer visible. Also we lose the drive to the relays, another heavy current consumer. This is not such a disadvantage however. How will the switched appliances operate during a mains failure? Unless of course someone invents a NiCad capable of opening a garage door by the time this gets into print! In the emergency situation the current requirement for the whole circuit does not exceed 50 mA and the NiCads can cope with this for quite a while. If the NiCads are to be replaced by nonrechargeable batteries R1 and D5 must

#### Front panel controls

This section can be taken as a small commercial for our front panels which are self adhesive, scratch proof and washable. They also bave that deep-down colour that will not fede if . . . but enough of that! On to the nitty-gritty!

be removed. These two components

provide a 'trickle charge' for the NiCads.

The layout of the front panel is illustrated in figure 2. The control functions (an upmarket term for keys and LEDs) can be decribed as follows:

4-44



MON THE WED THE FEE SAT SUN

: It may come as a surprise but these LEDs indicate the days of the week!

88-88 MENUT ● ● ● ●

The four digit time display. It is used to indicate the switching times during programming. (This is also true for the 'day' LEDs.) The centre LED flashes once a second.

: Four LEDs that indicate which of the four outputs is being programmed.

mur 🔘 🌘 🔘 These LEDs indicate the state of the outputs.

: This key will switch on one of the outputs during the programming procedure or when under 'manual' control. The key lights when a switching 'on' time is entered.

: As above, but for 'on' read 'off'!

: This key is used before entering a time 'period' in place of a specific switch off time. The LED will light to denote that the next entry parameter will be a period. : Similar to above but in this case it is a 'reset' time that will be entered and the

LED will prove the point.

The key used to start the clock or to switch the display back to real time

(after a programming entry for instance).

: Used to select a particular day during programming or when setting the clock.

: A switching output can be programmed by operating this key followed by 1, 2, 3 or 4.

This key is only used to get the clock out of the condition called 'single reset'. It will not be used very often and the reasons for this will be explained later.

Remember this key because it will wipe out the entire memory if operated in error. It will do exactly the same job if operated on purpose but without anything like the same sort of after effects!

: Deletes an incorrect key entry and clears the display.

1 2 WED

: These are dual function keys depending on the entry order of the specific program.

7 8 9 sun

DAILY

brane switches make it

possible for the keyboard and front panel to be combined into one. The

trensparent covers for the

display and LEDs are also part of the front panel.

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#### Operation

#### From the word go . . .

As soon as power is applied to the circuit the four displays will flash figure 8's and the 'second' LED stay out. The memory will of course be empty and the clock will not run. This condition will also occur whenever the mains supply is lost and no emergency battery backup has been included. Need we say more?

#### Starting of the clock

We get off to a good start by using, as an example, 17.30 on a Friday afternoon! Did you expect 08.00 on a Monday morning?

■ Press the TIME key : The MON LED will

■ Press DAY

· Press FR1

play will show 00.00. This is quite correct because the cycle of the clock begins at 00.00 on Monday and ends at 24.00 on Sunday. The 'seconds' LED will light but not flash. : The DAY LEDs will now start to

light and the dis-

flash. : The FRI LED will light continuously

3

## Parts List

#### Resistors

R1 = 270 Ω R2 = 39 k

R3 = 1 k R4 = 120 k R5,R6 = 1k5

R7,RB,R9 = 10 k R10 = 2k2 R11 = 4k7

R12 = 33 k R13...R20 = 33 Ω

R21 . . . R24 = 390 Ω P1 = 50 k preset

#### Capacitors.

C1 = 2200 u/25 V C2.C3.C4 = 22 µ/16 V C5,CB = 10 n C6 = 10 µ/16 V C7 = 100 n

C9 = 47 p C10 = 2µ2/16 V

#### Semiconductors: D1 = Rad LED

D2 . . . D5 = 1N4001 D6 ... D19.D39 ... D43 = IN414B D20 . . , D38 = Red LED,

3 mm ø D44 = Red LED

T1 ... TB = BC 547 IC1,IC3 = 7905 1C2 = 7908 1C4 = 7655IC5 = 74LS164 ICB - HM 6147 (Hitechi)

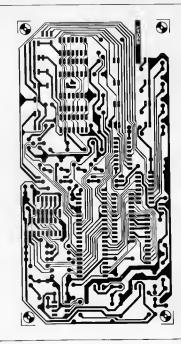
or 2147 (NEC) IC7 = TMS 1601 (Texas

Instrumentsk ICB, IC9 = ULN 2003

Miscellaneous: LD1 ... LD4 = 7760 B1 - Bridge rectifier B40C1500

Tr1 = Mains transformer 12 V/1,5 A Cabinet CL2 AFJ from

West Hyde Connector for keyboard contact strip evailable from Technomatic Limited



while the rest of the DAY LEDs will go out.

- Press the 1 7 3 0 keys in that order.
   This time will now appear on the display.
- Press the TIME key again and the displayed time will be entered into the memory.

The clock will now run — as indicated by the flashing 'second' LED. This will always be the case when 'real' time is displayed.

#### Time corrections

Changing the clock time (for British Summer Time for example) is almost identical to the initial starting procedure with the exception that the CLEAR key must be operated after the TIME key and before the DAY key.

#### Resetting

As stated previously, the clock cycle begins on Monday at 00.00 and ends on Sunday at 24.00. This can be altered if desired to another cycla period of less than seven days. If a reset time is entered the clock will, on reaching that time, 'jump' back to 00.00 Monday and repeat the shortened cycle again. Even this can be changed. If necessary.



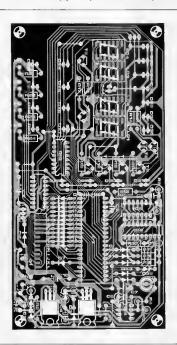


Figure 3, The keyboard is ettached to the printed circuit board by meens of e flexible flat connector strip, It is simply pushed into the edge connector on the printed circuit board.

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the clock can be made to stay at 00.00 Monday when it returns there. This is called the 'single reset' mode and can be achieved by the removal of D19 from the circuit. When the clock enters into the single reset mode it can only be restarted by pressing the start key.

The reset time is entered in the following manner (again using 17.30 Friday as an

example).

RESET DAY FRI 17.30 RESET. The 'reset' LED will remain lit for the period of time that the reset time is stored in the memory. The reset time can be erased from the memory by operating the RESET followed by the CLEAR MEM keys, The stored reset time can be displayed at will by operating the RESET key twice in succession.

#### Manual control of the outputs

It is a simple matter to control the outputs manually if necessary. If, for example, it is required to switch on output 1, the sequence

PROGR 1 ON and there it is! This output will remain on until another (manual or program) instruction arrives to change it. Pressing the TIME key will cause the normal time to reappear on the display.

#### Programming switching times

One point must be made quite clear before moving on any further with this section. As mentioned earlier, there are two 'types' of programming times. There is the daily cycle where the timing point occurs every dey. and there is the weekly cycle where the specific timing point may only occur once during the week (07.30 Friday for example). As mentioned earlier, these modes cannot be mixed in one program sequence! A further point to bear in mind is that if, by chance, the same output is switched at the same time in either the weekly cycle or the daily cycle, that output will just be 'inverted' from what it was, irrespective of what the programs said it should do. To program a switch-on time for output

I. kev in: PROGR 1 DAY FRI 1730 ON Two things may happen after pressing the

1. It is possible that the display disappears for e short period while the program is being written into memory. This is not a

problem and is no cause for alarm. 2. The number I output will be switched on if the time entered is earlier than the

time indicated on the display. Entering a 'daily' time is almost identical to the above program. In this case the DAILY key is operated instead of the 'day' (FRI) key. The sequence of events then, for output number 1 to switch off at 23.00 every evening, would be:

PROGR 1 DAY DAILY 2300 OFF Note that all 'day' LEDs will light when programming e 'DAILY' function.

## Programming hints

Point 2 mentioned above states that when programming switching times that are earlier than the time displayed at that moment, the output (or outputs) will switch on or off depending on the program instruction being entered. This can be avoided to a certain degree by entering the last switching time (that is before the actual time) first. The corresponding output will then remain in that condition.

It is not necessary to enter switching times in any particular order as the computer deals with this problem personally

Another tip! It is not necessary to operate the PROGR and DAY keys repeatedly when entering a sequence of switching times for one output. The following example will clarify this. It is required that output number 4 is to switch on at 7.00 and off et 12.30 on Wednesday. PROGR 4 DAY WED 700 ON

followed by: 1230 OFF

#### Period (PER) function

Using the PER key is an alternative method of indicating the switching off time. In the event that output number 3 is to be switched on at 9.00 on Monday and remain in this state for e period of 6 hours, the program would look like this

PROGR 3 DAY MON 900 ON and then:

600 PER the display will now show the switch off time that has been calculated by the computer and stored in memory (15.00 in the example here).

Displaying and clearing switching times Human memories, regretfully, do not measure up to the short term performance of 'solid state' memories. Therefore it is quite likely that a reminder may be required. In order to display the first programmed time for an output, this apparent

nonsense is entered: PROGR 1 PROGR PROGR

followed by: PROGR PROGR

This will cause all the switching times that are stored in memory for that output to be displayed in chronological order. If nothing has been entered for that output the displays will show 8's end all other

LEDs will light. Clearing the stored switching times is very similar to the ebove procedure. Now the CLEAR MEM key must be used whan the time in question appears on the displey. This time is then cleared and the processor will display the next switching time when the PROGR key is pressed once. If necessary, it is possible to clear the

whole program for a given output in one go. For output 1 for example: PROGR I CLEAR MEM

For really drastic measures, tha whole memory contents can be wiped out. CLEAR MEM CLEAR MEM . . . and there it was - gone!

#### Frrors

. . . and the last entry is keyed in to complete the program and . . . the display starts flasing 8's, the 'seconds' LED gives up and panic is

7-day timer/controller Elektor April 1983

Figure 4. This drawing

illustrates the constru

board, the front panel

of the cabinet end the

form one combined essembly.

self edhasive front panel

tion. The printed circuit

about to break out! It's difficult to decide whether to make a dive for the mains plug or lift the box up to stop it from scorching the table ton..!

Panic not, for what you are receiving is simply an 'error' indication. In this case an incorrect key was pressed (the last one of course) and this can be rectified by either pressing the correct one or using the CLEAR key and starting all over again, Don't you just know which method it will be!

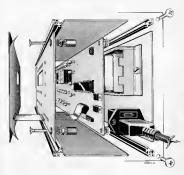
It is also possible for the displays to show 8's but the 'seconds' continue to function. This is a memory error or 'overflow' (caused by trying to fill the same memory area twice). Afraid it's back to the CLEAR key!

#### Construction

We really should have a short commercial here extolling the undoubted virtues of printed circuit boards, front panels and membrane-switch keyboards designed specifically for the job but we will forego

that for the moment. Instead we move on to figure 3 which Illustrates the component leyout for the printed circuit board. All the components shown in Figure 1 can be mounted on this board with the exception of the transformer, the NiCad cells, the relays and the keyboard.

4



Two components demand particular attention and these are the electrolytic capacitor CI and the bridge rectifier BI. These are mounted last, not on the front but on the trackside of the board. It will be necessary to place a layer of insulating tape between the body of the electrolytic and the board to prevent any untoward horrors from necuring.

Keyboards have a history of being rather difficult to mount proparly. There is all that soldering and that great gaping hole to cut which never seems to quite line up correctly (although it did when you

correctly (although it did when you started!). This time we have solved the problem—as if you didn't know by now! problem—as if you didn't know by now! the membrase switch keyhout consists of a number of leyers of plastic foll and consequently is very thin (a matter of a couple of milimetres). In fact, it is an integral part of the front panel. For a more detailed description of membrane switches refer to the consequence of the consequence of the in this issue.

The front panel, together with keyboard, is self adbesive and only needs to be placed on the front of the chosen cabinet and pressed bome. The keyboard contacts 'come out' on a flexible flat plastic strip which is attached to the printed circuit board by pushing the strip into a connector that is

mounted on the board.

mountee on the ocars. Figure 4 provides an indication of how the prototype locks. Construction is straight-forward and only requires the usual patience forward and only requires the usual patience as that the entire circuit can be effectively screened. This is done by connecting the case to the supply sero. It will be found preferable to use a case that enables the front panel to be removed as there are a number of holes that must be cut out. The CL2 AFJ case from West Hyde is ideal for the purpose as the front panel is both removable and exactly to the correct dimensions.

The following is a suggested sequence of events for construction:

- 1. Assemble the printed circuit board. 2. Cut the holes in the front panel for the display, the LEDs and the flexible connecting strip for the keyboard. Do not forget to drill and countersink the holes for the spacers that will carry the printed circuit board on the back of the front panel, This will be clear from the illustration in figure 4. Take care to ensure that the holes line-up with those in the printed circuit board. When the front panel is prepared, the spacers can be fitted using counter sunk screws. Care taken at this stage will handsomely repay the effort. It is not a bad idea to cover the inside on the front panel with a layer of insulating material to guard against the possi-
- With great care, fit the self-adhesive front panel in plece and apply a little pressure over all its surface.

bility of short circuits.

- Now it is time to screw the printed circuit board in position.
- Finally the transformer and the NiCad cells can be fitted in the interior of the

7-day timer/controller Elektor April 1983

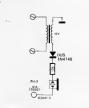


Figure 5. Fully monaural calibration is used to set the frequency of the emergency stendby power supply. The only other requirement is one human

cabinet. The relays or solid state switches can also be mounted now if it is intended that they are to live with the rest of the circuit.

If ordinary relays are to used it is recommended that an RC network consisting of a 100  $\Omega$ /I we restroot in series with a 100  $\alpha$ /S0 V is wired accoss their contacts to avoid interference coused by 'sparks'. The race of the cabine will also require a few holes made for the manus supply lead and the switching outputs. Do not insert 1C 4... 8 into their sockets until the construction of the printed direut board is complete. The construction of the printed direut the construction of the printed direct that the complete of the construction of the printed in the construction of the printed direct the construction of the printed direct that the construction of the printed and the conrect when R1 and D5 are fitted and IC4 and IC7 are not.

When the board is ready connect the transformer, switch on, and check the voltage levels from the supply. The preset potentiometer PI can now be calibrated (see the calibration chapter for this). If everything is as it should be, R1, D5 and the ICs can be fitted to the board.

a. Mount the LEDs so that they do not quite touch the front panel when the board is fitted in position. The displays can be brought nearer the front panel by mounting them in two sockets in 'piggyback' fashion under each display.

Connections to the board by wires carrying the supplies and outputs to the relays and so on can best be made using printed circuit pins soldered to the track side of the board. This will allow them to be reached quite easily later on. The voltage regulator ICI must be mounted

The voltage regulator ICI must be mounted in some convenient position to use the case as a heat sink. If this is not possible than a heatsink will be required, Be sure to use an insulating mica washer.

b. The position of the contact strip keyboard at the back of the front panel is shown in dotted lines in figure 2. This drawing can be used as a pattern when cutting the holes in the cabinet front panal. Remember to make an opening for tha contact strip.

c. It may be found easier to cut the front panel to the exact size before fitting it in place on the cabinet front panal. Obviously a sharp knife will be needed for this purpose. Do not cut off more than 10 mm from the top or bottom edges and not more than 20 mm from the sides.

#### Calibration

The astable multivibrator (1C4) will always be synchronised to the mains frequency during normal operation. If an emergency standby supply is not to be used then calibration of P1 will not be necessary. Setting P1 to its mid position will be fine. If, on the other hand, an emergency backup is fitted then IC4 must be calibrated to provide an output frequency of 50 Hz. even in the absence of a mains supply. Obviously this calibration will be a simple matter with the aid of a frequency counter. However, a reasonably accurate setting can be acheived without it, All that is required in the way of test equipment is an ordinary crystal earpiece or piezo tweeter. It happens like this:

- For safety's sake remove all the ICs from the printed circuit board with the exception of IC4 and the voltage regulators.
- Disconnect the power from the mains.
   Connect the earpiece or tweeter as shown
- in figure 5.

  Connect the batteries or NiCads for the
- emergency supply.

  Rotate P1 until the tone from the earpiece

is as pura as it is possible to get, without any obvious 'beat'. That completes the calibration procedure! The batteries must again be disconnected before mounting the ICs on the board.

#### Final remarks

This last chapter could be entitled 'Quirks' because that is precisely what it is about. Not real problems, just peculiarities that we found with the prototype.

Here is the first. It may happen that the display goes off for some seconds when a lot of program entering is being carried out and the TIME key is presed. This is quite normal and is due to the fact that when the processor is very buy; it will tend to 'forget' about the display for a short period of time. The clock does in fact begin to run as soon as the TIME key is operated.

A second peculiarity. When checking switching times being stored in memory by displaying them, it is possible that the time displayed does not correspond to that being stored. There is one feasible explanation for this. The clock switched at the instant the time checks were being performed. In that case, it is certain that nothing has changed in the mamory! The correct information can be displayed by first pressing the CLEAR key and then continue with the required procedure for checking stored switching times. Last but not least. The display will flash for one full minute at 00.00 (except for Sunday at midnight). This was considered

junior program tester elektor march 1983

P Wild

You have written your own program. Now it is merely necessary to entar the program and run it. In many cases however, the bugs are having a field day. Where does one start with debugging? The first items to check are the contents of the accumulator, the X- and Y-register and the processor status register. This is usually the quickest way of finding possible errors.

# junior program tester

a valuable aid in writing programs The register check is not very complicated and is a great aid to all Junior Computer owners. In order to display the contents of the accumulator and of the X- and Y-register only a few commands are needed; these are contained in the program and ensure that the contents of the accumulator and of the X- and Y-register are displayed from left to right, A few commands are also contained in the program for displaying the processor status register. Something more is required, bowever. In addition to the software a small hardware change is needed; figure 1 shows the circuit. Since there is insufficient space on the display for hexadecimal representation of the processor status register contents, the flag statuses are displayed bit-bybit (there is not much point in indicating the status of the individual flags in hexadecimal notation). The program loads the contents of the processor status register into port A. An 8-bit data latch, IC1, accepts the logic states of the individual flags and displays them using drivers N1 ... N7 and LEDs D1 ... D7.

The circuit of figure I should be constructed on an additional board which can then be plugged into the port connector when needed. The program to be tested is provided with a BRK command at a suitable point, and the BRK vector must point to the start of the following test program:

|   | 85 | FB |    | STA POINTH |  |
|---|----|----|----|------------|--|
|   | 86 | FA |    | STX POINTL |  |
|   | 84 | F9 |    | STY INH    |  |
|   | 68 |    |    | PLA        | Fetch processor<br>status register and         |
|   |    |    |    |            | load it into port i                            |
|   | 8D | 80 | 1A | STAPAD     |  |
|   | A9 | FF |    | LDA # FF   |  |
|   | 8D | 81 | 1A | STA PADD   | Port A output                                  |
|   | 8D | 83 |    | STA PBDD   | Port 8 output =<br>clock signal for<br>FF1 FF7 |
| r | 20 | 8E | 1D | JSR SCANDS |  |
|   | 4C | XX | XX | JMP        | Back to /                                      |

The only escape from the test program is via RST.

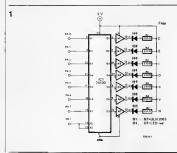
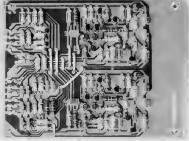


Figure 1. Two IC's, seven resistors end seven LEDs; the total hardwere investment. Prelude part 3 elektor epril 1983

(Part 3)

## sophistication!): a tone defeat switch, enabling the filter circuit to be taken out so that the tone controls have no effect on the signal. elude



tone control

The first question that demands an answer is: Why do we have a tone control for the Prelude? After all, it was stated at the outset that we would do our utmost to keep the 'processed' signal as pure as possible and now we are suddenly discussing tone controls. What have they to do with high quality reproduction? Not a lot! However, situations can arise where a tone control may prove rather useful. Of course, e tone control is superfluous if everything else is 100% OK from the record right through to the speakers. Unfortunately, the real world is something else end there is often a need for some subtle corrections in the frequency response

A case in point is that of older records. A great many collections include records that date back ten or more years. These very often leave something to he desired in terms of quelity and it is here that a tone control circuit hecomes fairly important. We make no comment about the average quality of records hought today!

A second argument in favour of tone controls comes to light when that eqe-old topic of speakers-versus-rooms is broached. Very few listeners do not now realise that the ecoustics of the room have a significant effect on the speaker system in use. Inadequacies can be compensated for by the educated use of the tone circuit. To a certain degree, a 'cost-effective' speaker can

The end is in sight! We have now reached the final constructional article in the

Prelude series: the printed circuit board for the tone control. This circuit is not entirely essential for a control emplifier but it can be useful. This is especially true if it has switchable turnover points for the filters. And (the height of

> also he given a helping hand. It is also true to say that not every listener's hearing is the same, especially if years are beginning to tell. We now have a situation where a tone control is very necessary. And let's not forget Mr Average, a member of the vast majority who likes to hear what he likes to hear - especially the hass! And whet is

wrong with that, mey we ask? When all is said and done then, we feel that most people would welcome the tone control circuit here. The added facility of the two switched turnover points for both the high and the low frequencies make it quite useful. The 'straight wire' fanetics have the tone defeat switch to satisfy them, When operated this, es its name suggests, takes all the tone control elements out of circuit.

#### The tone control in detail

The complete circuit diagram of the tone control is shown in figure 1. This again follows the concept of the line and headphone amplifiers (T1 . . , T9), The detailed description of the amplifier circuit itself will therefore he found in the articles covering the line and headphone amplifiers (February and March 1983). A lightning trip through the circuit goes like this:

The input is fed to T1 which, together with T2, makes up a differential amplifier with a current mirror (T4 and T5) in its collector lines. The signal is then fed to the output stage consisting of T8 and T9 via the darlington pair, T6 and T7. This configuration provides considerable gain. The dc current setting of the differential amplifier is taken care of by T3, while T10 looks efter the darlington (and therefore the output stage). These two transistors are connected as current sources. The input signal is fed to the base of T1 while the feedback arrives at the base of T2. Part of the tone control network is included in the feedback loop.

It is now time to take a little side-step into the tone control circuit because it is slightly different from that usually expected. The most frequently used tone control circuit is the Baxandall. This is en active circuit that makes use of the feedback loop of the amplifiers. Its principle of operation is illustrated in figure 2a. A frequency dependent potentiometer P forms the feedback loop of the amplifier: the input signal is present et one side of the potentiometer and the output signal at the other. If we take the impedance of the left part of the potentiometer as Z1 and the impedance of the right side as Z2, then the gain will be  $\frac{Z2}{Z1}$ . This means that

the input end output signals will have the same value when the wiper of the potentiometer is in the mid position. It follows then that rotating P clockwise will increase the impedance of Z1 by the same amount Preiude pert 3 elektor april 1983

Figure 1. The circuit diagram of the tone control for the Prelude. The basis of the circuit is a high quality discrete opamp that has been used in other sections of the Prelude preamplifier.



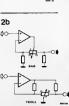
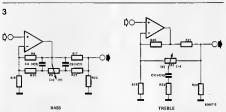
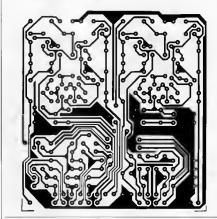


Figure 2. The usual configuration of a Baxandall tone control network is illustrated in figure 2a. The circuit in the Pratude is a modified form of this (figure 2b). The major difference is that the first requires a buffer stage and the second doesn't.



TV .. T3', T8', T1 0' + BC 980C

Figure 3. This figure shows those components that are included in the high and low fraquency control circuits. The component numbers in breckets refer to the capacitors that are switched.



#### Parts list

Resistant R1.R1' = 82 k R2,R2',R9,R9',R17. R17' = 220 k R3,R3',R7,R7' = 2k7 R4 R4' = 2k2 R5.R5'.R6.R8' = 4k7 R8,R8',R15,R15' = 330 \( \Omega\) R10,R10',R11 B11' = 100 Ω R12,R12',R14,R14',R16, R16', R23,R23' = 1 k R13,R13' = 22 k R18 R18' R19 R19' R24 B24' = 1 M R20,R20',R21,R21' = 6k8 R22.R22' = 220 Ω P6,P6' = 50 k (47 k) IIn stereo potentiometer\* P7 P7' = 10 k lin, stereo

#### potentiometer\* Cepacitors

C1,C1'= 330 n C2,C2'= 120 p C3,C3':C5,C5',C14, C14'= 22 \(\mu/10\) V C4,C4'= 33 p C6...C9'= 10 n C10,C10',C11,C11'= 68 n C10,C10',C11,C11'= 68 n

C13' = 100 n Semiconductors: T1,T1',T2,T2',T3,T3',

T8,T8',T10, T10' = BC550C T4 . . . T7,T4' . . . T7', T9.T9' = BC560C

Miscellaneous: S4 = 4-pale, 2-way\* S5 = 2-pole, 2-way\*

\* mounted on bus board

that 22 decreases. Depending on the (capacitive or inductive) characteristic of P, a certain part of the frequency range will be amplified more. However, if the potentionneter is rotated in the opposite direction the amplification factor will be reduced. The overall tone control circuit must have a very low impedance in order to avoid sty notice problem of the properties of the propertie

Our solution to the problem is presented in figure 2b. This shows that the frequency dependent potentiometer is now placed behind the opamp. The oparetion of this circuit is practically identical to the Baxandall tone control. If the potentiometer is rotated so the value of Z1 increases and that of Z2 decreases, this would lower the amplification factor while the output attenuation would be bigber. The reverse is of course true. This circuit has one disadvantage when compared to the usual Baxandall configuration. The output must supply a slightly higher output voltage to compensate for the output attenuation. This could mean that the amplifier will start copping at higher input signal levels but this problem will not occur

if the presets at the input of the Prelude are correctly set.

The practical circuits for both the high and the low frequency tone controls are shown in figure 3. It will be noted that a number of components lead a 'double life'.

Back to the main circuit diagram of figure 1. The switched turnower points are provided by switches S4 (low frequencies, 400 Hz and 800 Hz) and S5 (high frequencies, 2 kHz and 4 kHz). Each switch changes the value of the frequency determining capacitors by adding a further capacitor in parallel.

The tone defeat switch, \$12, is not shown in the circuit diagram of the tone controls. Since it has nothing to do with the tone controls it appears on the circuit diagram of the bus board. For the detailed description of this board refer to the March 1985 issue. The tone defeat switch does just that by selecting either the input or output of the tone control circuit. If the tone defeat switch does just that by witch is 'ni, the input of the tone control circuit. If the tone defeat switch does just the put of the tone control circuit is grounded and is therefore prevented from supplying an output timal.

#### Construction

The printed circuit board for the tone control module is shown in figure 4. There are a number of points that require particular attention during construction.

The switches S4 and S5 and the stereous



Figure 4. The printed circuit board of the tone control stage. There are two rows of connecting points that must be connected to the bus board by short wire links.

potentiometers P6/P6' and P7/P7' are connected directly to the bus board with the aid of short lengths of wire. The next step requires a little more patience. There ere two sets of connecting links between this printed circuit board end the bus board. The row nearest the edga of the board are connected on the component side of the board while the row farthest from the edge of the board are connected on the track side. Short lengths of wire (resistor offcuts!) ere soldared in place so that they stick-out from their allocated side of the board. Those on the component sida can ba bent to run parallel with the board. The same will beppen to those on the track sida but they must first be covered with e short length of sleeving to prevant any nestias heppening. Take note that the component side of the tone control board faces towards the right when looking at the front of the Preluda, A little patience is needed when fitting the 'edge connector' into the mother board to ensure that problems do not arise.

The Prelude is now complete and testing can begin. If visitors are present – prepare to act nonchalant! A signal source will of course be required and a tunar will be suitable for this purpose. An empfilter and a pair of speakers would also be convenient! After checking to see that all switches and controls are as they should be – just switch on!

This is an extremely dangerous moment! As soon as the Prelude bursts into life - es of course it will - your visitor will suddenly realise that an electronics genius is standing right in front of him. Without hesitation, he will proceed to off load en assortment of tapa recorders that don't. televisions that sometimas do and a micro-miniature radio that was fine before it 'fall' in the bath! Than there is this battery razor that keeps running down and . . . Next month wa will bave e short article concerning 'last-word' points of note and things! Not to be missed! Meanwhile, enjoy a pleasant period of trying out your favourite records on the Prelude, Don't forget to listen to the music sometimes . . .

## programmable darkroom timer

convenient IC allows simple construction



Meny amateur photographers will be interested in this circuit: a programmable timer for seven preselectable times. The time can be adjusted in two renges (up to 99.9 s and up to 999.s). Fourteen keys ere used for programming. When the start key is pressed. the time countdown can be observed on

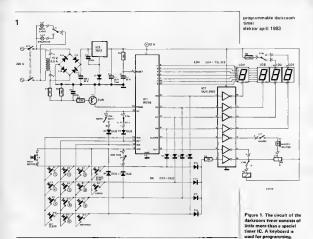
the displey. The preset value then reeppears on the displey. There is therefore no need for repeated entering of the proper exposure time.

The advantages of a programmable disk room times are well known. Most manifacturers now try to make their equipment as simple to use as possible. An LED diplay and the second of the second transparent transpare

#### The timer IC: WD 55

Since the circuit of figure 1 chiefly consists of timer IC WD 55, we shall examine the characteristics of this IC in some detail. The manufacturers describe their product as e versatile timer module which can replace all mechanical timers in control engineering. Without wishing to dispense free publicity, we can nevertheless say that this IC is ideally suited to our application.

The device is a 4-bit microprocessor with onchip (preprogrammed) ROM. In other words, all the characteristics described in the following are already provided, except for a few which are selected externally. The processor can operate in two modes: as leay programmable timer/controller, the insemal RAM is used as data memory and a 4-digit. RAM is not as data memory and a 4-digit. In its other mode, the processor operator as an on/off timer: writeless or diodes serve as data memory and data readout; there is therefore no need for a display.



Incidentally, the principle of such a timer was presented in the 'vider ange darkroom timer' in the October 1981, issue. The two modes are selected by means of a diode between pin 38 and pin 15 of the IC. If the diode is omitted, as is the case in this application, the processor operates as a key programmable inters/sequence switch. In this mode a keyboard and a 4 digit, 7-segment display are required. The processor can then be utilised as an ideal darkroom timer. Figure 1 shows the complete circuit.

#### Programming

The timer is programmed via a keyboard erranged in a 4 x 4 matrix. Table 1 shows the functions assigned to the individual keys. The cross-hatched boxes show that certain functions are or can be permanently selected with these connections. Fourteen keys are needed to make the

times freely programmeble. Each keystroke is acknowledged by a beep from the piezo-electric buzzer. Thus it is also possible to use a sensor type, touch-keyboard. However, one imperfection is associated with this key-ecuated operation: the operation is only initiated when the key is released (except for the stop-function). But one quickly becomes accurational to this.

The key functions in detail:

ADVANCE: this key is used to step through the memory (RAM) stages. There are seven stages altogether, allowing several different times to be programmed. With each keystroke, the memory number on the extreme

Table 1

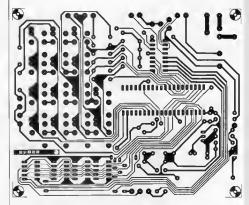




Table 1. Functions of the timer IC when used as a derkroom timer.

Outputs: pin 19 – relay pin 25 – piezo-electric fuzzer programmable darkroom timer elektor april 1983

2



#### Ports list

#### Resistors:

H1,H2,R4,R5 = 10 k

R3 = 33 k R6 = 470 Ω P1 = 10 k preset

#### Capacitors:

C1 = 220 µ/25 V C2 = 1 µ/25 V tentelum C3,C5 = 330 π C4 = 1 µ/25 V

#### Semiconductors:

D1...D13 = DUS D14...D17 = 1N4001

#### LD1 ... LD4 = TIL 313 (common cathode) T1 = TUN

IC1 = WD-55 Pronto Electronics (01-599-3041) IC2 = ULN 2003 IC3 = 7812

#### Miscelleneous:

Tr = mains transformer 15 V/0,5 A sec. S1 ... S14 = Digitast SR S15 S16 = double-pole

changeover switch S17,S18 = single-pole switch

S19 = double-pole meins switch 8z = piezo-electric buzzer

(e.g. PB 2720) Re = 12 V Siement pcb relay V23027-A0002 (electrovatue)

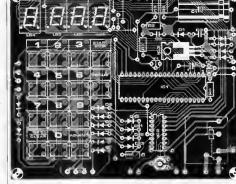


Figure 2. All the components of the derkroom timer can be mounted on this p.c. board (except the meins transformer, piezo-electric buzzer and lamps). left of the display (LD4) is incremented by Simultaneously the memory contents appear on the three right-hand positions of the display (LD1 . . . LD3). The most significant position is displayed by LD3, A '7' et LD4 is followed by e 'I' when the key is pressed.

SET/CLEAR: pressing this key prepares the processor for data input into the memory via the numeric keys '0' . . . '9'. The display is set to '000'. The entered data are displayed step by-step from right to left. If e numeric key is pressed for the fourth time, this value eppears in LD1 end so on. Entry is terminated by pressing env non-numeric key. The display reedout is automatically stored end there is therefore no need for an ENTER or DATA STORE key. Protection against incorrect entries is provided by the fect that the processor only responds to a numeric key if the SET/CLEAR key has previously been pressed.

FOCUS: this key can be used to switch the relay on and off at the output of the processor. This facility makes it possible to adjust the enlarger before sterting the exposure. The relay remains in that condition until this key is pressed egain, or until the exposure is started by pressing the START

START/STOP: as its name implies, this key is used to stert or stop the exposure. Countdown of the displayed time begins when the key is released. An exposure in progress is immediately stopped by pressing this key. Care should be taken to press the key only briefly, otherwise the process starts egain after a certain delay. The relay operetes and the program is sequenced to the next programmed time. The START/STOP key can elso be bridged with e footswitch, and is the only key which is operational when en exposure is in progress.

Numeric keys 0 . . . 9: deta can be entered with these keys. The SET/CLEAR key must previously have been pressed. The entered data ere shown on the display from right to left. The number just entered is shifted ecross the display from right to left and overwrites the previous number. The keys can be pressed any number of times. However, only the last three entries are displayed and stored. Unless the SET/CLEAR key has first been pressed, pressing the numeric keys will have no effect.

Apart from this free programming using the keys described, some functions can be permenently selected using switches, Diode D4 sets the internal timebase to e clock frequency of 50 Hz.

Diode D5 is switched on with switch S18 if the processor is only to process one time. When this time bas elapsed, therefore, sequencing does not take place to the next programmed time. Instead, LD4 continuously displays a 'l' and when the time hes elapsed LD1 . . . LD3 display the previously selected time once again. The ADVANCE key is not operational in this case. Via S15a, diode D3 establishes whether or

not the time can be programmed in the 1 s or in the 0,1 s scale. This results in a display range of either 1 . . . 999 s or 0.1 . . . 99.9 s. programmable darkroom

timer elektor april 1983

\$15b switches over the decimal point. Via S16e, diode D2 esteblishes whether the programmed times are to run eutomatically or manually. Automatic means that when the start key is pressed, times 1 . . . 7 will run successively. This automatic run only stops when the seventh time has elapsed. The first time is then displayed egain and the keyboard is operational once more, In this case the reley is switched off with \$16b An example of an application using this mode is its use as a process timer in a photogrephic laboretory. If one selects a time of 10 min et position 5, 0.5 min at position 6, and 3 min at position 7, the developer bath, the stop bath end fixer bath for the film can be timed in that sequence starting with time 5. The times should always be arranged so that the last time to be programmed is et position 7. Otherwise the timer continues to run and one can lose treck of the times to

ba monitored. The buzzer emits e beep 10 s before e programmed time elapses. When the time has elapsed, the buzzer beeps twice.

#### Darkroom timer

The description of the programming already contains all the information needed to operete the darkroom timer. The additional hardware is shown in figure 1. This circuit consists of little more than the timer IC, keyboard, display, diodes and switches for fixed programming. IC1 can directly drive the segments of the display. The cathodes of the 7-segment readouts are driven via 1C2. Of the remaining three drivers in IC2, two control e piezo-electric buzzer (this can be switched off with S17, end P1 is used to edjust its volume) end one drives the relay for switching the two lamps (enlarger end derkroom lighting). Display outputs

D0...D3 are connected to the keyboard via diodes end are elso routed to the buzzer in an OR operation. This results in a beep with each keystroke.

R4 and C4 et pin 8 of IC1 ensure that the display is automatically reset to '1 000' when the operating voltage is switched on. The 50 Hz signal for the timebase reaches the timer IC via R1 . . . R3, C5 end T1. The power supply with voltage regulator IC3 provides the 12 V operating voltage. Although IC3 becomes very warm, it does not require e heatsink. After insertion, this IC is bent so that the metal surface faces upwerds.

The printed circuit board eccepts elmost the entire circuit (figure 2). The only external connections required are from the transformer and buzzer to the p.c.b. end from the relay contact to the lamps. If desired, a footswitch can also be connected in parallel with

> Literature. WD-65 Industrial Timer/Controller, Western Digital Composition

The 'Talking clock' presented by Elektor in the June 1982 issue only speaks when instructed to do so. In order to hear the time, it is therefore necessary to press the talk button. The following extension makes the circuit more convenient and the time is announced every hour.

## talking-clock extension

| Teble 1          | Teble 1. Minutes ones |   |   |   |   |  |
|------------------|-----------------------|---|---|---|---|--|
| Digit            | Signal M.O            |   |   |   |   |  |
|                  |                       | b |   | f | g |  |
| 0                | 1                     | 1 | 1 | 1 | 0 |  |
| 1                | 0                     | 1 | 0 | 0 | 0 |  |
| 1<br>2<br>3<br>4 | 1                     | 1 | 1 | 0 | ŧ |  |
| 3                | 1                     | 1 | 0 | 0 | 1 |  |
| 4                | 0                     | 1 | 0 | 1 | 1 |  |
| 5                | 1                     | 0 | 0 | 1 | 1 |  |
| 6                | 1                     | 0 | 1 | 1 | 1 |  |
|                  | 1                     | 1 | 0 | 0 | 0 |  |
| 8                | 1                     | 1 | 1 | 1 | 1 |  |
| 9                | 1                     | 1 | 0 | 1 | 1 |  |

| Oight | Sı | Signel M.T |   |  |  |
|-------|----|------------|---|--|--|
|       | d  |            | f |  |  |
| 0     | 1  | 1          | 1 |  |  |
| 1     | 0  | 0          | 0 |  |  |
| 2     | 1  | 1          | 0 |  |  |
| 3     | 1  | 0          | 0 |  |  |
| 4     | 0  | 0          | 1 |  |  |
| 5     | 1  | 0          | 1 |  |  |

E.A.J. Bogers

The speech generator IC (IC1 in figure 4 of the Elektor June 1982 issue) receives its time information via flip/flops IC2 . . . IC5. This is the information for the minutes and minute tens, hours and hour tens. The information for the minutes and minute tens which are present at the control inputs of the speech generator IC are listed in tables l and 2. At every full bour (at XX.00) the minutes information M.O.f and M.O.q is '1-0' and at M.T.e and M.T.f it is '1-1'. These logic signals only appear in this combination once in the tables. It is therefore merely necessary to start the speech generator IC with this information combination. The signals required are designated as follows in the circuit diagram (figure 4 of June 1982 issue): 'H' for M.T.f, 'G' for M.T.e, 'E' for M.O.f and the inverted signal 'A' for M.O.g. The inverted signal 'A' is available at pin 9 of IC5. The four signals are subjected to a logic operation via the AND-gate of figure 1. The output of gate N1 is e logic 1 when the minutes indicate

00. The RC network between gates N1 and N2 suppresses the small interfering pulses created by the IRQ routine of the clock. As a result of the feedback provided by R3, gate N2 acts as a Schmitt trigger. Should this more talkative clock become irritating, it can be silenced by setting switch S1 to position b'. The connecting points G, H, E, pin 9 of ICS, ALRM, 15 V and

artitating, it can be signized by setting swin.

SI to position 'b'. The connecting points

G, H, E, pin 9 of ICS, ALARM, +5 V and

O V in figure 1 are connected to the corresponding points on the speech board (for example, +5 V to the positive terminal of C13).

One minor imperfection must be taken into account. The time is announced if the date is queried on the hour and the clock is then switched back to the time display. As soon as the display indicates XX.00 again, the automatic time announcement becomes operational. The adjusting of switching times can also result in time announcements. In order to suppress this, switch S1 should simply be set to position by.

S ALARM

S A

Figure 1. This is the circuit for eutometic hourly ennouncements of the time 1

#### Multiminiswitch

Adding to the wide range of applications streedy in existence for rotary wafer switches, N.S.F. have taken a miniature rotary wafer switch and an ultre-ministure pushbutton switch, forming them into one duel action component

Basing this arrangement upon a concentric sheft, the outer controls the rotery movement and activation of a model MX, 30° indexing switch whilst the inner provides the linear movement necessary to operate the UMO momentary action nushbutton switch



The length dimension from the mounting face to the end of the pushbutton body is 34 mm and the diameter of the MX wafer is a mare 17 mm.

Applications for such a versatile dualaction switch includes the control of electronic latching circuits.

NSF, Limited, Keighley. Yorkshire 8D1 5EF. Telephone: 05352.61144

(2562 M)

#### MEC 75 - mini super switch The MEC 76 switch module has been

developed in order to provide a printed circuit board mounting switching module with a very high degree of versatility. In a package measuring only 15 mm x 15 mm no less then 6 different contact functions ere possible simply by selecting the

appropriate pins with the printed circuit board design. The MEC 75 is designed to fit the standard 0.1 inch grid patrern and is available in both momentary and elternete key action, both with tactile



meximum versatility by providing buttons and bezels for the switch, dezels can accompdate 1 or 2 high brightness 'pinhead', or rectangular bar LEDs, or without LED illumination as desired. The bezels are designed to allow for maximum mounting density in any configuration, using a 0.1 inch grid printed circuit board. Particular attention has been paid to the aesthetic design of the keycap and bezel combination and they are available in 10 attractive colours as standard.

The Unimed keydan system again allows

The Vario-Support system is a variable mounting frame that enables the switch renge to be panel mounted and eliminates the necessity of a printed circuit board. The Vario-Support system is available in any cell combination from 1 x 1 up to a 10 x 10 matrix.

MEC 75 54 Poplar Grove. Maidstona Kent ME16 OAN Telephone: 0622 674947.



#### Slimline Counter

Instant installation, regardless of voltage, is available with the latest electronic counter developed by Trumeter. Their new similine counter is a universal totalising counter, which is only 16 mm thick and is reedy for surface mounting, requinng only two small panel mounting holes. It has flying lead connections and, without adjustment, is suitable for a supply voltage range from 6 V DC up to 240 V AC, using a battery or mains supply.



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Count rate is up to 50 counts per second and the counter has high noise immunity CMOS logic. Operation is entirely noiseless and the LCD display is easily readable in embient light conditions.

Trumeter Company Ltd. Milltown Street, Radcliffe Manchester M26 9NX Telephone: 061 724.6311.

#### (2618 M) 'Flat' analogue panel meters

(2617 M)

The new Tableau series announced by Anders Electronics Limited represents a fundamental re-think of analogue pensi meter design. Not just a cosmetic alternative to traditional design, but a major change of approach - 'flat'meters without

harrafe To accommodate the size of traditional movements, virtuelly all previous penal meter case designs have included a rear barrel projection. The resulting complexities of a panel cut-out and the restrictions imposed by the intrusion of the barrel into the equipment interior have confronted design and production engineers over the years. By storting with a completely new movement design, the Tehleau series avoids these disadventages.

At the heart of the meter is a new ultrathin 'sandwich' type moving coil movement. Built around a rugged teut-band suspension, this movement is seid to meet or exceed the performence of most earlier designs and will withstand shock testing st 200 G. The natural low-friction properties of the taut-band approach leads to an overall linearity better then 1.5%. Standard sensitivities are from 100 µA

# market



upwards and special renges or scales are evallable on request.

The sim style case is just 14 mm thick and has a flat back surface, accept for or and has a flat back surface, accept for two terminels which serve as mounting or 10 mm dismeter clearance holes are needed in the host penel to accommodate the terminals, which are elactrically solded from the panil. The Tableau series is initially available in three sizes: 49 mm or 10 km 2 mm and 8 km 2 mm and

Anders Electronics Ltd., 48-56 Bayham Place, London NWI DEU. Telephona: 01 387 9092 (2622 M)

#### Tiny rotary pre-set switch

The N.S.F. rotary pre-set switch is a compact, switchable link for snap mounting into printed circuit boards and operated by a standard 3 mm screwdriver. The houng size is only 10 mm dis. x 3.5 deep. The design enables accustion from aither end of the rotor thus affording equipment designers a high degree of flexibility, especially



where operation is from outside the equipment via an access hole in the casing. Two and three position models are available rated at 100 mA 50 V.

N.S.F. Limited, Kaighlay,

Yarkshire, BD21 5EF. Telephane: 0535.61144. (2626 M)

### New power plug helps computers

Microcomputers suffering from emnesiaused no longer be terminal cased

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Power International Ltd., 2A Isambard Brunel Road, Portsmouth, Heats

. Telephone: 0705.756715.

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#### multimeter

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and presentation combined with wideranging specifications in 57 ranges and features 50,000 V protection on all ranges. With voltage measurements to 1000 V IOC and ACI and current to 30 A [DC and ACI together with resistence and capacitance ranges, the "Super Electro" also



provides phase sequence detection for three phase systems and a main and instilation tester. Based on a high precision, moving coil incroammeter, the Super Electro' has coloured scales, an antihigh quality lined protective case, fused test leads and a years guarantee With SCKEIV sensitivity and OZ occurrey of 1.5% LsJ., the 'Super Electro' offers acceptional sensitivity and OZ oscuracy of CS 39.85

Black Star Ltd., 9A Crown Street, St. Ives, Huntingdon,

Huntingdon, Cambs PE17 4EB. Telephone: 0480 62440.

(2619 M)

#### Solenoid operated cassette mechanism

In deference to many requests from both home computer enthusissts and professional data logging equipment menu-



facturers for a low cost OC controllable cassette machanism, Ambit is launching a range of such mechanism at the forth-coming All Electronics Show. The illustrated mechanism, is the AMBTN-3600, and can be supplied with a variety of

phase response is not optimised. Toko's thick-film low pass filters are available

standard version is equipped with a stereo record/pleyback and erase head, although a wide range of combined record/replay heads are also available. Motor operation is from 12 V OC, with less than 0.08% RMS wow and flutter, with the record and playback specification largely datermined by the heeds selected and the drive circuitry provided, Ambit also offers a renge of multi-cepstan, multi-motor decks for high performance applications.

Ambst Internetional 200 North Service Road. Brantwood, Essux CM14 4SG.

(2620 M)

#### Small illuminated pushbutton switches

A new range of small illuminated pushbutton switches and matching indicators is now available from N.S.F.

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Normal colour coding of actuators is based upon 5 colours -- red, yellow, green, blue and white which can be illuminated with an incandascent lemp. But combinations of red, green or yellow LED with differing filter can provide a wide variety of visual affects e.g., a single element red LED when used with a vallow diffusing cap produces a tight brown affect.

NSE Limited Karahiav

Vocksburg 8021 55F Telephone: 0535,61144

[2827 M]

#### Thick film filters

Toko have introduced a range of 20 kHz low pass active filters, specifically for the digital audio market: the PAL 0900 serves, The imminent arrival of the Digital Audio Disk has focussed much attention on the A/D and D/A designs required, but the low pass filter is probably one of the most critical points from the serious 'hifi' listener's point of view - since the perfection of the PCM process can be completely squandered at the simple LPF stage if the



with a variety of terminating impedances, of 30 AWG (0.25 mm) wire. Refill spools ell with carefully optimised group delay, and with stophend attanuations up to -95 dB. Samples, date and applications edvice is everlable from Ambit.

Ambir International 200 North Service Road.

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OK have introduced a battery powered version of the Just Wrap tool, the 6JW-3. In the patented Just Wrap process, a specially designed bit compresses insu-

lated wire against the wrap post so that the post edge cuts through the insulation and makes reliable contact with the wire conductor. This allows the user to wrep directly from a wire real or spool without precutting and prestripping, and makes it possible to wire continuous strings across any number of points with a single continuous insulated wire. The string may be ended at any point with a built in cut-off mechanism. The new BJW-3 is powered by two rechargeable batteries and features e rugged ABS housing and herdened steel components for performence and dura-

It comes complete with a specially designed bit and sleeve, and a 100 ft (30 m) spool

are available in 100 ft (30 m) lengths and in 4 colours, blue, red, white end yellow OK Machina & Tool (UK) Ltd.,

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(2628 M)

Telephone: 0703.610944. 12625 MI

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